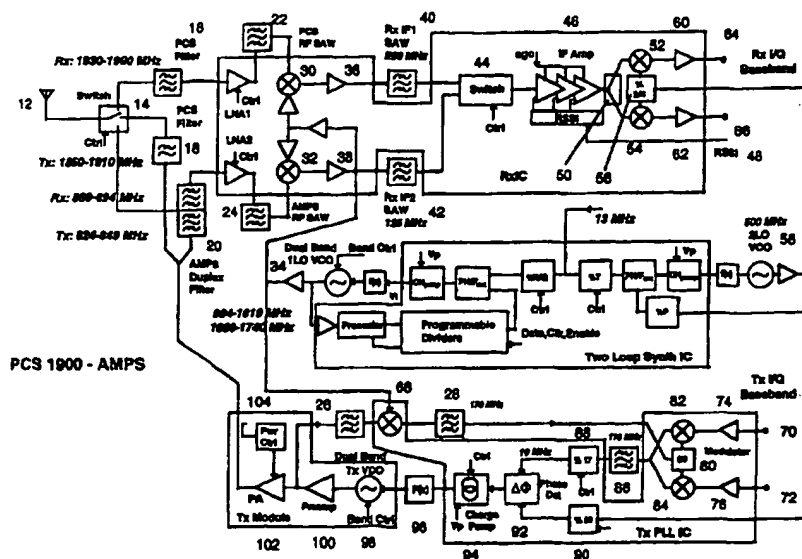




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(54) Title: A DUAL-MODE RADIO ARCHITECTURE



(57) Abstract

This invention relates to a multi-mode radio transceiver. The transceiver is operable, in one embodiment, to communicate with digital and analogue modulation format cellular radio systems. The handset utilises a novel frequency plan and architecture to enable a minimum level of complexity for dual-mode operation. The frequency plan preferably uses two local oscillators 1LO, 2LO to generate all the required frequencies for both the digital and analogue modulation format transceivers, derived from a single reference crystal oscillator. A radio architecture is used to enable switchable dual-mode operation with maximum commonality of functional blocks to facilitate silicon integration.

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A DUAL MODE RADIO ARCHITECTURE

Field of Invention

- 10 The present invention relates to a dual mode radio architecture and, in particular, relates to the same for use in a mobile radio handset.

Background Art

- 15 Personal communication networks are being deployed extensively world-wide using mobile radio systems. Early cellular networks, still in operation, use analogue modulation formats for the radio air interface protocol. These analogue networks exhibit the problem of call saturation in high usage areas. The North American AMPS system is typical of
20 such an analogue system.

- To overcome this problem higher capacity air interface protocols using digital modulation format networks have been introduced, oftentimes operating in tandem with analogue networks, providing cellular radio
25 coveragethat by both systems. Examples of digital mobile phone networks currently in operation are the PCS 1900, DCS 1800 and GSM systems, which are national and supra-national standardised radio air interface protocols (AIPs).

- 30 Nevertheless, these networks exhibit the problem of not having global operation. To overcome this problem, additional global radio air interface protocols using digital modulation format networks via satellite have been introduced, such as the digital ODYSSEY system. Furthermore, short range cordless networks, such as the digital CT2
35 version of the US unlicensed UPCS band, or DECT are becoming increasingly employed.

In a large country such as the United States or Canada the early standardised analogue network known as AMPS has reached a fairly

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universal coverage of the populated North American continent. The newer digital networks tend to be deployed in areas of high usage. A result of this is that there are areas of digital network coverage overlaying a universal analogue network coverage. Additionally different air interface protocol standards of digital networks have been deployed regionally, since different telecommunications operators have developed their own protocols or have developed such protocols in line with national and sometimes international standards authorities, for example, the GSM protocol.

10

Whilst it is reasonable to suppose that handsets operable for different radio communications protocols are similar from the users point of view, it is not possible, in particular, to use a digital mobile radio for use in an analogue cellular region and vice versa. This stems from the fact that whilst both types of handsets possess antennas, radio front end transmitter, receiver and baseband circuits, they operate on different air interface protocols with different radio carrier frequencies, duplex timing and modulation formats, which are incompatible.

15

Therefore it can be seen that each individual personal communications system user will need a dual network service for complete coverage. Consequently the user requires a handset that will not only function throughout the coverage area of the specific subscribed-to digital network, but also have a switched alternative mode to operate on the universal analogue network.

20

There is a likely deployment scenario where the mobile terminal user wishes to access the two communication operating systems. Therefore there is a need for a dual-mode PCS1900 / AMPS, PCS / UDCS, PCS / ODYSSEY, DCS1800 / GSM, GSM / DECT, DCS1800 / DECT, JDC / Handyphone and JDC / ODYSSEY handsets, to name but a few.

25

The problem of implementing a dual mode handset has been considered to be surmountable by two different approaches: The first solution uses two separate radio transceivers piggybacked and combined at the antenna and at the man-machine interface (keyboard and audio); The

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mode operation can be achieved by operating each of the required RF front-ends from a specific first local oscillator.

5 The higher frequency radio system local oscillator bands can be arranged to overlap, by switching intermediate frequencies, whereby the tuning range is constrained to avoid retuning between transmit and receive.

10 Preferably the transceiver is provided with a means to determine the operational mode. Alternatively, switch means may be provided to effect a change in operational mode.

15 In accordance with another aspect of the present invention, there is provided a radio transceiver comprising a combination of functional block circuits and frequency plan,

wherein, in a first circuit, disparate radio air interface signals are downconverted, with a first local oscillator, and filtered with switched separate intermediate frequency filters, amplified and converted using a second local oscillator, to in-phase and quadrature baseband signals, in
20 a common sub-system of functional blocks,

wherein, in a second circuit, disparate baseband modulation format in-phase and quadrature signals are upconverted to their respective disparate radio air interface signals in a common subsystem of functional blocks configured as a phase lock loop upconverting modulator, which
25 uses a common first local oscillator to the receive circuit and a reference derived from the prime second local oscillator,

wherein, the frequency synthesis is arranged so that only two phase locked voltage controlled oscillators are required, with the two receive second local oscillator frequencies integer relationship derived from the
30 prime second local oscillator, itself phase locked to the frequency reference, and the first local oscillator frequencies all derived from the same frequency reference, with the higher frequency radio system local oscillator bands arranged to exactly overlap, by switching intermediate frequencies, so as to constrain the tuning range to avoid retuning
35 between transmit and receive.

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second solution uses two separate radio sections piggybacked and combined at the digital signal processing part of the radio transceiver.

5 These two approaches have problems in that they are complicated and unwieldy, and it is clear that a dual-mode radio architecture with an increased functional commonality of circuits would be the most cost effective solution.

Object of the Invention

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The present invention seeks to provide a dual mode radio architecture.

Statement of Invention

15 In accordance with one aspect of the present invention, there is provided a radio front end transceiver operable to receive and transmit radio signals in different frequency bands and modulation formats, the transceiver comprising; receive and transmit paths for each modulation format, wherein common receive and transmit intermediate frequency
20 circuitry is employed and the local oscillator requirements for the intermediate frequency to baseband conversion are derived from a single frequency synthesiser.

25 Preferably, the first local oscillator requirements for the intermediate frequency to radio frequency conversion are derived from a single frequency synthesiser.

30 Preferably, a single first local oscillator is used and the required dual mode operation is achieved by using a dual band voltage controlled oscillator and a programmable synthesiser. The first local oscillator can employ overlap tuning in order to constrain the voltage controlled oscillator tuning range appropriate for the dual band.

35 The transceiver can comprise a single second local oscillator, wherein the required dual mode operation is achieved by integer division of the second local oscillator to derive the required local oscillator input signals. Two first local oscillators can be used and the required dual

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The present invention also provides a handset incorporating a dual band transceiver.

5 In accordance with another aspect of the invention, there is provided a method of operating a radio front end transceiver operable to receive and transmit radio signals in different frequency bands and modulation formats, wherein for each modulation format the same receive and transmit intermediate frequency circuitry is employed and the local oscillator requirements for the intermediate frequency to baseband
10 conversion are derived from a single frequency synthesiser.

In accordance with another aspect of the invention, there is provided a method of operating a radio front end transceiver operable to receive and transmit radio signals in different frequency bands and modulation
15 formats, wherein for each modulation format the same receive and transmit circuitry is employed and the local oscillator requirements for the radio frequency to intermediate frequency and intermediate frequency to baseband conversions are derived from a single frequency synthesiser.

20 In accordance with a still further aspect of the invention, there is provided a method of operating a mobile radio handset operable to receive and transmit radio signals in different frequency bands and modulation formats, wherein for each modulation format the same receive and transmit intermediate frequency circuitry is employed and the local
25 oscillator requirements for the intermediate frequency to baseband conversion are derived from a single frequency synthesiser.

In accordance with a yet further aspect of the invention, there is provided a method of operating a mobile radio handset operable to receive and
30 transmit radio signals in different frequency bands and modulation formats, wherein for each modulation format the same receive and transmit circuitry is employed and the local oscillator requirements for the radio frequency to intermediate frequency and intermediate frequency to baseband conversions are derived from a single frequency synthesiser.

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Brief Description of Drawings

In order that a greater understanding of the invention be attained, embodiments of the invention will now be described with reference to the
5 accompanying drawings, wherein:-

Figure 1 depicts a typical handset outline schematic,

Figure 2 is a detailed implementation of a PCS1900 -
AMPS dual mode radio front end;

10 Figure 3 is a frequency plan of a PCS1900 - AMPS dual
mode radio front end;

Figure 4 is a detailed implementation of a PCS1900 -
UPCS(CT2) dual mode radio front end;

15 Figure 5 is a frequency plan of a PCS1900 - UPCS(CT2)
dual mode radio front end;

Figure 6 is a detailed implementation of a PCS1900 -
Odyssey dual mode radio front end;

Figure 7 is a frequency plan of a PCS1900 - Odyssey dual
mode radio front end;

20 Figure 8 is a detailed implementation of a DCS1800 -
DECT dual mode radio front end;

Figure 9 is a frequency plan of a DCS1800 - DECT dual
mode radio front end;

25 Figure 10 is a detailed implementation of a DCS1800 -
GSM dual mode radio front end;

Figure 11 is a frequency plan of a DCS1800 - GSM dual
mode radio front end;

Figure 12 is a detailed implementation of a GSM - DECT
dual mode radio front end;and

30 Figure 13 is a frequency plan of a GSM - DECT dual mode
radio front end.

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Detailed Description

Figure 1 shows an outline block diagram of a typical cellular radio handset. Radio frequency signals are received and transmitted by the antenna 2 which is connected to a radio front end 4. In the radio front end transmit and receive signals are converted between radio frequency and base band, whereby digital signal processing means 6 encode the transmit and decode the receive signals and from these can determine the audio signals which are communicated to and from the handset user by loudspeaker 7 and microphone 8. The front end will typically contain transmit and receive paths which are mixed to an intermediate frequency with a local oscillator. These intermediate frequency signals will be further processed and mixed so that the input and output signals to and from the front end are at baseband and suitable for digital to analogue or analogue to digital conversion, as appropriate, prior to digital signal processing.

Referring now to Figure 2, there is shown one embodiment of the present invention, comprising a dual mode radio front end for the reception of both digital PCS1900 signals and analogue AMPS signals. PCS1900 operates in the frequency band 1930 to 1990 MHz on the receive downlink to the handset and in the 1850 to 1910 MHz band on the transmit uplink from the handset. AMPS operates in the frequency band 869 to 894 MHz on the receive link from the basestation to the handset and in the 824 to 849 MHz band on the transmit link from the handset to the basestation.

The PCS1900 handset operates either in a transmit mode or in a receive mode: AMPS can operate in both modes simultaneously. For this reason the switch 14 from the antenna 12 has three positions. Band pass filter 16 and lowpass filter 18 are provided on the input and output lines for the PCS1900 signals whilst a single duplex filter 20 is employed for the input and output lines of the AMPS signals. Low noise amplifiers LNA1 and LNA2 are provided on the input for PCS1900 and AMPS modes. Power amplification is provided by a dual band PA.

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- For PCS1900 and AMPS separate band filters 22 and 24 are provided from the outputs of the LNAs to the inputs to the mixers 30 and 32. Similarly separate IF channel filters are provided 40 and 42. Preferably these filters are radio frequency surface acoustic wave SAW devices.
- 5 These SAW devices can be fabricated on quartz, as is known. The advantages provided are such that the separate receive bands can be converted down to an IF and separate channel selection performed, so that over each RF band a large number of channels can be determined .
- 10 Turning now to the receive path for the digital PCS1900 signals, when the switch 14 directs incoming digital PCS1900 signals to the PCS1900 receive path, the signals from the band select filter 22 are passed to a mixer 30 which mixes the received signal with a signal from a synthesised local oscillator 34 to produce an intermediate frequency (IF)
- 15 signal at 250 MHz which is subsequently amplified by further amplifying means 36. The PCS1900 signals are then filtered by the 250 MHz IF channel filter, and the filtered IF signal is then passed through a second switching circuit 44 which operates simultaneously with the first switch 14 by a mode control means (not shown). The mode control means
- 20 identifies whether the signals are PCS1900 or AMPS modulation and determines in which mode the transceiver is operating.

- The receive signal output from switch 44 is fed to an IF amplifier with automatic gain control and a receive signal strength indicator (RSSI) 48.
- 25 After amplification, the signal path is routed through splitter 50 and the signals are output to a mixer pair 52 and 54, and after mixing with a quadrature 250 MHz signal derived through switchable division in 56 from a synthesised 500 MHz second local oscillator (2LO), in-phase and quadrature baseband signals are amplified by amplifiers 60 and 62, to
- 30 provide output signals at 64 and 66 to be fed to the analogue to digital converters and digital signal processing means (not shown).

- If an analogue AMPS radio signal were present at the antenna and a decision made to receive that signal, the switch 14 would feed the signal
- 35 from the antenna 12 through duplex filter 20, amplifier LNA2, filter 24 to mixer 32. At mixer 32 the radio frequency signal is downconverted, using a synthesised local oscillator to a further intermediate frequency

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(IF) of 125 MHz which is different from that of the digital PCS1900 case. This IF signal is subsequently amplified by amplifying means 38 before channel selection by the 125 MHz filter means 42. The 125 MHz IF signal is then passed through the switching circuit 44. As in the case
5 with the digital PCS1900 signal, the analogue AMPS IF signal is output through the common IF amplifier, which, after amplification and mixing with a quadrature 125 MHz local oscillator signal derived through switchable divider 56 from the second local oscillator (2LO), in-phase and quadrature baseband signals are provided at 64 and 66 to be fed to
10 the analogue to digital converters and digital signal processing means (not shown).

The custom receive IC uses separate PCS1900 and AMPS LNA/down converters with external SAW image filters. Separate SAW intermediate
15 frequency filters at 250 MHz and 125 MHz are used for PCS1900 and AMPS. For both systems the IF filtering is arranged to be for a single channel.

For transmit, the PCS1900 and AMPS baseband signals are raised to a
20 common 170 MHz intermediate frequency (IF) within a phase lock loop (PLL) which effectively converts the 170 MHz IF to any selected channel in either the PCS1900 or AMPS RF bands. The baseband signals, derived from digital signal processing and digital to analogue converters (not shown), are input at ports 70 and 72, and are amplified by amplifiers
25 74 and 76 prior to upconversion to the phase lock loop IF of 170 MHz in the quadrature modulator combination of mixers 82 and 84. The upconverted modulation at the 170 MHz IF containing either the PCS1900 or AMPS signals is filtered 86 and applied to the divider 88 prior to being fed to the PLL phase detector. The reference input to the
30 phase detector is derived by a divider 90 from the prime second local oscillator 58. A charge pump 94 feeds from the phase detector to the loop filter 96, whose output controls the dual-band VCO 98. The phase lock loop is closed and effectively channel tuned by filtering 26 a sample of the preamp 100 output, and downconverting to the 170 MHz IF via the
35 mixer 68 using the same synthesised first local oscillator 34 as for receive. This IF is filtered by filter 28 and applied in the loop to the

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modulator at the 90 degree phase shifter 80. The transmit PLL intermediate frequency filters will not need to be of high shape factor.

5 In terms of the frequency generation or local oscillator requirements for this dual mode radio, It can be seen from Figures 2 and 3 that all the required local oscillator frequencies are derived from two voltage controlled oscillators, referenced via two phase lock loops to 13.0 MHz.

10 With reference to the radio frequency plan of Figure 3, it can be seen that the first local oscillator signals are derived from a single dual band voltage controlled oscillator (VCO). Band selection operation of the first local oscillator VCO in the frequency band 1680 to 1740 MHz for the digital PCS1900 case or in the frequency band 994 to 1019 MHz for the analogue AMPS case is used and four intermediate frequencies (IFs)
15 result. The two receive IFs have been arranged to be integer related to the 500 MHz prime second local oscillator (2LO) and to be at frequencies convenient for SAW channel filter implementation. The two transmit IFs have been arranged to be the same frequency and reducible by integer division to a 10 MHz reference frequency.

20

The quadrature signal local oscillators required to mix the two receive IFs down to baseband are readily derived by division from the 500 MHz 2LO. For the PCS1900 receive state the 1930 to 1990 MHz band is downconverted to a 250 MHz IF using a channel tuning synthesised 1LO
25 covering the range 1680 to 1740 MHz. The channel filtered 250 MHz IF is downconverted to baseband in-phase and quadrature signals using a 250 MHz quadrature local oscillator derived from the 500 MHz 2LO.

30 For the PCS1900 transmit state, in-phase and quadrature baseband signals are upconverted to a 170 MHz signal using the 170 MHz IF derived within the phase lock loop by downconversion with the channel tuning receive local oscillator from the RF output. The 170 MHz IF is divided and phase locked to a 10 MHz reference and this output controls the transmit RF VCO frequency via a charge pump and loop filter.

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For the AMPS receive state the 869 to 894 MHz band is downconverted to a 125 MHz IF using a channel tuning synthesised 1LO covering the

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range 994 to 1019 MHz. The channel filtered 125 MHz IF is downconverted to baseband in-phase and quadrature signals using a 125 MHz quadrature local oscillator derived from the 500 MHz 2LO.

- 5 The AMPS transmit state is similar to the PCS1900 in that in-phase and quadrature baseband signals are upconverted to a 170 MHz signal using a 170 MHz IF derived within the phase lock loop by downconversion with the channel tuning receive local oscillator from the RF output. The 170 MHz IF is divided and phase locked to a 10 MHz
10 reference and this output controls the transmit RF VCO frequency via a charge pump and loop filter.

- A feature of the 1LO synthesised frequency generation is that the transmit and receive local oscillator tuning bands are arranged to
15 overlap by using different transmit and receive IFs. In this way the overall tuning range of the 1LO for PCS1900 can be constrained such that transmit to receive switching can be achieved in an allowable time. It is a requirement in the AMPS case for the transmit and receive first local oscillators to tune the same range and channel as the AMPS radio
20 simultaneously transmits and receives. In this dual-mode it has been arranged that for the PCS1900 case the transmit and receive first local oscillators exactly overlap also.

- The two receive quadrature second local oscillator signals are derived
25 from the prime 500 MHz 2LO as follows. The prime second local oscillator provides a synthesised 500 MHz signal 58 which is fed to a switchable divide by two or divide by four divider 56 resulting in a 250 MHz or 125 MHz local oscillator. For the transmit path the 500 MHz is divided by 50 in divider 78 resulting in the 10 MHz reference.

30

- Referring now to the local oscillators, they receive a 13.0 MHz signal which is processed through dividers set by control signals: this allows the local oscillators to operate in the PCS1900 and the AMPS transmit or receive modes. Associated with the first local oscillator, there are band
35 control means operable to adjust the local oscillator frequency in response to channel detection means which detect the channel employed in any communication. In the case of the mobile initiating a

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call then the channel detection means will be able to detect which channels are available and be able to effect the control switches for the PCS1900-AMPS circuits as appropriate. The programmable dividers operate under the control of data, clock and enable commands. The
5 prime second local oscillator 2LO produces a fixed frequency signal at 500 MHz, and employs a feedback path to ensure that the frequency is maintained. By these means the required frequencies 250, 125 and 10 MHz can be derived from the 13.0 MHz reference, as can the required first local oscillator frequencies.

10

The synthesiser first local oscillator accomplishes dual mode operation by having a dual band local oscillator which tunes a single band for transmit and receive either 1680 to 1740 MHz for the PCS1900 or 994 to 1019 MHz for AMPS transmit and receive. In the case of PCS1900 the
15 1LO tunes in 200 kHz spaced channels and for AMPS in 30 kHz channels, defined by the by R/Q divider on the reference to the phase lock loop, and the programmable divider.

Referring now to Figure 4, there is shown a second embodiment of the
20 present invention, comprising a dual mode radio front end for the reception of both digital cellular PCS 1900 signals and digital cordless UPCS(CT2) signals. UPCS(CT2) operates in the frequency band 1920 to 1930 MHz and in the same frequency channel on the receive and transmit links by way of time division duplexing.

25

The PCS 1900 handset operates either in a transmit mode or in a receive mode and uses frequency division duplex: UPCS(CT2) also operates in transmit or receive mode but uses the same frequency channel with time division duplex. For this reason the switch 14 from the
30 antenna 12 has three positions. Band pass and lowpass filters 16 and 18 are provided on the input and output lines for the PCS signals whilst a second switch 106 and single RF band filter 20 are employed for UPCS(CT2) signals. Low noise amplifiers LNA1 and LNA2 are provided on the input lines respectively. Power amplification is provided by a
35 single band power amplifier PA continuously covering the PCS1900 band so that the UPCS(CT2) band which falls between the PCS1900 transmit and receive bands is also covered.

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For PCS1900 and UPCS(CT2) separate band filters 22 and 24 are provided from the outputs of the LNAs to the inputs to the mixers 30 and 32. Similarly separate IF channel filters are provided 40 and 42.
5 Preferably these filters are radio frequency surface acoustic wave SAW devices. The advantages provided are such that the separate receive bands can be converted down to an IF and separate channel selection performed, so that over each RF band a large number of channels can be determined .

10

The receive path for the digital PCS1900 signals, including the receive signal output from switch 44, is as described for the first embodiment). The mode control means identifies whether the signals are PCS1900 or UPCS(CT2) modulation and determines in which mode the transceiver is
15 operating.

If a UPCS(CT2) radio signal were present at the antenna and a decision made to receive that signal, the switch 14 would feed the signal from the antenna 12 through band filter 20, amplifier LNA2, filter 24 to mixer 32.
20 At mixer 32 the radio frequency signal is downconverted, using a synthesised local oscillator to a further intermediate frequency (IF) of 125 MHz which is different from that of the digital PCS1900 case. This IF signal is subsequently amplified by amplifying means 38 before channel selection by the 125 MHz filter means 42. The 125 MHz IF signal is then
25 passed through the switching circuit 44. As in the case with the digital PCS1900 signal, the UPCS IF signal is output through the common IF amplifier, which, after amplification and mixing with a quadrature 125 MHz local oscillator signal derived through switchable divider 56 from the second local oscillator (2LO) 58, in-phase and quadrature baseband
30 signals are provided at 64 and 66 to be fed to the analogue to digital converters and digital signal processing means (not shown).

The custom receive IC uses separate PCS1900 and UPCS LNA/down converters with external SAW image filters. Separate SAW intermediate
35 frequency filters at 250 MHz and 125 MHz are used for PCS1900 and UPCS. For both systems the IF filtering is arranged to be for a single channel.

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For transmit, the PCS1900 and UPCS baseband signals are raised to separate 170 and 125 MHz intermediate frequency (IF) within a phase lock loop (PLL) which effectively converts the IF to any selected channel in either the PCS1900 or UPCS(CT2) RF bands. The baseband signals, derived from digital signal processing and digital to analogue converters (not shown), are input at ports 70 and 72, and are amplified by amplifiers 74 and 76 prior to upconversion to the phase lock loop IF 170 or 125 Mhz in the quadrature modulator combination of mixers 82 and 84. The upconverted modulation at the 170 MHz IF containing either the PCS1900 or UPCS signals is filtered 86 and applied to the divider 88 prior to being fed to the PLL phase detector. The reference input to the phase detector is derived by a divider 90 from the prime second local oscillator 58. A charge pump 94 feeds from the phase detector to the loop filter 96, whose output controls the dual-band VCO 98. The phase lock loop is closed and effectively channel tuned by filtering 26 a sample of the preamplifier 100 output, and downconverting to the 170 or 125 MHz IF via the mixer 68 using the same synthesised first local oscillator 34 as for receive. This IF is filtered 28 and applied in the loop to the modulator at the 90 degree phase shifter 80. The transmit PLL intermediate frequency filters will not need to be of high shape factor.

In terms of the frequency generation or local oscillator requirements for this dual mode radio, it can be seen from Figures 4 and 5 that all the required local oscillator frequencies are derived from two voltage controlled oscillators, referenced via two phase lock loops to 13.0 MHz.

With reference to the radio frequency plan of Figure 5, it can be seen that the first local oscillator signals are derived from a single dual band voltage controlled oscillator (VCO). Band selection operation of the first local oscillator VCO in the frequency band 1680 to 1740 MHz for the digital PCS1900 case or in the frequency band 1795 to 1805 MHz for the UPCS(CT2) case is used and four intermediate frequencies (IFs) result. The two receive IFs have been arranged to be integer related to the 500 MHz prime second local oscillator (2LO) and to be at frequencies convenient for SAW channel filter implementation. The two transmit IFs have been arranged to be at frequencies reducible by

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integer division to a 10 or 12.5 MHz reference frequency, both themselves derived by integer division from the 500 Mhz prime second local oscillator (2LO).

- 5 The quadrature signal local oscillators required to mix the two receive IFs down to baseband are readily derived by division from the 500 MHz 2LO. For the PCS1900 receive state the 1930 to 1990 MHz band is downconverted to a 250 MHz IF using a channel tuning synthesised 1LO covering the range 1680 to 1740 MHz. The channel filtered 250 MHz IF
10 is downconverted to baseband in-phase and quadrature signals using a 250 MHz quadrature local oscillator derived from the 500 MHz 2LO.

- For the PCS1900 transmit state, in-phase and quadrature baseband signals are upconverted to a 170 MHz signal using the 170 MHz IF
15 derived within the phase lock loop by downconversion with the channel tuning receive local oscillator from the RF output. The 170 MHz IF is divided and phase locked to a 10 MHz reference and this output controls the transmit RF VCO frequency via a charge pump and loop filter.

- 20 For the UPCS receive state the 1920 to 1930 MHz band is downconverted to a 125 MHz IF using a channel tuning synthesised 1LO covering the range 1795 to 1805 MHz. The channel filtered 125 MHz IF is downconverted to baseband in-phase and quadrature signals using a 125 MHz quadrature local oscillator derived from the 500 MHz 2LO.

- 25 The UPCS(CT2) transmit state is similar to the PCS1900 in that in-phase and quadrature baseband signals are upconverted to a 125 MHz signal using a 125 MHz IF derived within the phase lock loop by downconversion with the channel tuning receive local oscillator from the
30 RF output. The 125 MHz signal is divided and phase locked to a 12.5 MHz reference and this output controls the transmit RF VCO frequency via a charge pump and loop filter.

- A feature of the 1LO synthesised frequency generation is that the
35 transmit and receive local oscillator tuning bands are arranged to overlap by using different transmit and receive IFs. In this way the overall tuning range of the 1LO for PCS1900 can be constrained such that

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transmit to receive switching can be achieved in an allowable time. It is a requirement in the UPCS(CT2) case for the transmit and receive first local oscillators to tune the same range and channel, as the UPCS(CT2) radio uses the same air frequency for uplink and downlink. The 1LO is
5 arranged to have a 1680 to 1805 MHz tuning range to cover the requirements of both PCS1900 and UPCS(CT2). In this dual-mode it has been arranged that for the PCS1900 case the transmit and receive first local oscillators exactly overlap also.

10 The two receive quadrature second local oscillator signals are derived from the prime 500 MHz 2LO as follows. The prime second local oscillator provides a synthesised 500 MHz signal which is fed to a switchable divide by two or divide by four divider 56 resulting in a 250 MHz or 125 MHz local oscillator. For the transmit path the 500 MHz is
15 divided by 50 in divider 90 resulting in the 10 MHz reference, or it is divided by 40 in divider 90 to give a 12.5 MHz reference.

Referring now to the local oscillators, they receive a 13 MHz signal which is processed through dividers set by control signals: this allows the local
20 oscillators to operate in the PCS1900 and UPCS(CT2) modes.

In the case of the mobile initiating a call then the channel detection means will be able to detect which channels are available and be able to effect the control switches for the PCS1900-UPCS(CT2) circuits as
25 appropriate. The programmable dividers operate under the control of data, clock and enable commands. The prime second local oscillator 2LO produces a fixed frequency signal at 500 MHz, and employs a feedback path to ensure that the frequency is maintained. By these means the required frequencies 250, 125, 12.5 and 10 MHz can be
30 derived from the 13 MHz reference, as can the required first local oscillator frequencies.

The synthesised first local oscillator accomplishes dual mode operation by having a local oscillator which tunes a single band from 1680 to 1805
35 MHz which covers the PCS requirement for transmit and receive of 1680 to 1740 MHz and the UPCS(CT2) requirement of 1795 to 1805 MHz. In the case of PCS1900 the 1LO tunes in 200 kHz spaced channels and for

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UPCS(CT2) in 100 kHz channels, defined by the by R/Q divider on the reference to the phase lock loop, and the programmable divider.

Referring now to Figure 6, there is shown one embodiment of the present invention, comprising a dual mode radio front end for the reception of both digital PCS1900 signals and digital ODYSSEY signals. ODYSSEY operates in the frequency band 2483.5 to 2500.0 MHz on the receive link from the satellite to the handset and in the 1745.5 to 1761.5 MHz band on the transmit link from the handset to the satellite.

The PCS1900 handset operates either in a transmit mode or in a receive mode: ODYSSEY can operate in both modes simultaneously. For this reason the switch 14 from the antenna 12 has three positions. Band pass filters 16 and lowpass filter 18 are provided on the input and output lines for the PCS1900 signals whilst a single duplex filter 20 is employed for the input and output lines of the ODYSSEY signals. Low noise amplifiers LNA1 and LNA2 are provided at the receive inputs. A separate LNA downconverter GaAs MMIC is provided for the ODYSSEY mode to meet a noise figure requirement not achievable in silicon.

For PCS1900 and ODYSSEY separate band filters 22 and 24 are provided from the outputs of the LNAs to the inputs to the mixers 30 and 32. Similarly separate IF channel filters are provided 40 and 42. Preferably these filters are radio frequency surface acoustic wave SAW devices. The advantages provided are such that the separate receive bands can be converted down to an IF and separate channel selection performed, so that over each RF band a large number of channels can be determined .

Turning now to the receive path for the digital PCS1900 signals, including the receive signal output from switch 44, is as described with reference to the first embodiment. The mode control means identifies whether the signals are PCS1900 or ODYSSEY modulation and determines in which mode the transceiver is operating.

If an ODYSSEY radio signal were present at the antenna and a decision made to receive that signal, the switch 14 would feed the signal from the

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antenna 12 through duplex filter 20, amplifier LNA2, filter 24 to mixer 32. At mixer 32 the radio frequency signal is downconverted, using an output from the additional synthesised local oscillator to a further intermediate frequency (IF) of 225 MHz, which although having the same centre
5 frequency as PCS1900 case, has a channel bandwidth commensurate with the ODYSSEY mode. This IF signal is subsequently amplified by amplifying means 38 before channel selection by the 225 MHz filter means 42. The 225 MHz IF signal is then passed through the switching circuit 44. As in the case with the digital PCS1900 signal, the ODYSSEY
10 IF signal is output through the common IF amplifier, which, after amplification and mixing with a quadrature 225 MHz local oscillator signal derived through switchable dividers 56 and 68 from the second local oscillator (2LO), in-phase and quadrature baseband signals are provided at 64 and 66 to be fed to the analogue to digital converters and
15 digital signal processing means (not shown).

PCS1900 uses the custom silicon LNA/down-converter on the receive IC with external SAW image filters. While ODYSSEY uses a separate custom GaAs MMIC for the LNA/down-converter. For both systems the IF
20 filtering is arranged to be for a single channel.

For transmit, the PCS1900 and ODYSSEY baseband signals are raised to 135 MHz intermediate frequencies (IFs) in both cases. These baseband signals, derived from digital signal processing and digital to
25 analogue converters (not shown), are input at ports 70 and 72, and are amplified by amplifiers 74 and 76 prior to upconversion in the quadrature modulator combination of mixers 82 and 84, using one of two local oscillators, at 135 MHz for PCS1900 and ODYSSEY, derived through switchable division in 78 and 80 from the 1350 MHz synthesised second
30 local oscillator. The upconverted IF containing either the PCS1900 or ODYSSEY signals at 135 MHz is combined 100 and applied to the switch 286 and then to the transmit IF filter combination 288 and 290 in order to select the required bandwidth of IF. Either of the two IFs are then upconverted in mixers 292 or 294 to the PCS1900 transmit band at 1850
35 to 1910 MHz and the ODYSSEY transmit band at 1745.5 to 1761.5 MHz. The respective signals are RF band filtered by 226 and 228 prior to

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power amplification and then fed to the antenna via separate filters and switch 14.

5 In terms of the frequency generation or local oscillator requirements for this dual mode radio, it can be seen from Figures 6 and 7 that all the required local oscillator frequencies are derived from three synthesisers, referenced to 13.0 MHz.

10 With reference to the radio frequency plan of Figure 7, it can be seen that the first local oscillator signals are derived from two 1LO synthesisers, themselves referenced to a 13.0 MHz crystal reference (not shown). The two receive IFs have been arranged to be the same frequency and integer related to the 1350 MHz prime second local oscillator (2LO) and to be at frequencies convenient for separate SAW channel filter
15 implementation. The two transmit IFs have been arranged to be the same frequency and an integer function of the prime second local oscillator by division and mixing. The prime second local oscillator is also referenced to the 13.0 Mhz crystal reference.

20 The quadrature signal local oscillators required to mix the two receive IFs down to baseband are readily derived by integer division from the 1350 MHz 2LO. For the PCS1900 receive state the 1930 to 1990 MHz band is downconverted to a 225 MHz IF using a channel tuning synthesised 1LO covering the range 1705 to 1765 MHz. The channel filtered 225 MHz IF
25 is downconverted to baseband in-phase and quadrature signals using a 225 MHz quadrature local oscillator derived from the 1350 MHz 2LO.

For the PCS1900 transmit state in-phase and quadrature baseband signals are upconverted to a 135 MHz IF using a 135 MHz quadrature
30 local oscillator derived from the 1350 MHz 2LO. The filtered 135 MHz IF is upconverted to the 1850 to 1910 MHz band using a channel tuning synthesised 1LO covering the range 1715 to 1775 MHz.

For the ODYSSEY receive state the 2483.5 to 2500.0 MHz band is
35 downconverted to a 225 MHz IF using a channel tuning additional synthesised 1LO covering the range 2258.5 to 2275.0 MHz. The channel filtered 225 MHz IF is downconverted to baseband in-phase and

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quadrature signals using a 225 MHz quadrature local oscillator derived from the 1350 MHz 2LO.

The ODYSSEY transmit state is similar to the PCS1900 in that in-phase
5 and quadrature baseband signals are upconverted to a 135 MHz signal
using 135 MHz quadrature local oscillator derived from the 1350 MHz
2LO. The filtered 135 MHz IF is upconverted to the 1610.0 to 1626.5 MHz
band using a channel tuning synthesised 1LO covering the range 1745.0
to 1761.5 MHz. It can be seen that the ODYSSEY transmit 1LO has been
10 arranged to be provided by the same 1LO as that of PCS1900.

A feature of the 1LO synthesised frequency generation is that the
PCS1900 transmit and receive local oscillator tuning bands are
arranged to overlap by using different transmit and receive IFs. In this
15 way the overall tuning range of the 1LO for PCS1900 can be constrained
such that transmit to receive switching of the 1LO can be achieved in an
allowable time. In the ODYSSEY case the transmit and receive first local
oscillators are separate due to the 873.5 MHz duplex spacing.

20 The two receive quadrature second local oscillator signals are derived
from the prime 1350 MHz 2LO as follows. The prime second local
oscillator provides a synthesised 1350 MHz signal which is fed to a
divide by three divider 68 resulting in a 450 MHz feed to the divide by
two divider 56, which feeds quadrature 225 MHz oscillator signal to the
25 mixers 52 and 54.

Similarly for the transmit chain the two transmit quadrature second local
oscillator signals are derived from the prime 1350 MHz 2LO by a divide
by five divider 78 resulting in a 270 MHz feed to the divide by two divider
30 80, which feeds quadrature 135 MHz oscillator signal to the mixers 82
and 84.

Referring now to the local oscillators, they receive a 13.0 MHz signal
which is processed through dividers set by control signals: this allows the
35 local oscillators to operate in the PCS 1900 or ODYSSEY transmit or
receive modes

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In the case of the mobile initiating a call then the channel detection means will be able to detect which channels are available and be able to effect the control switches for the PCS1900-ODYSSEY circuits as appropriate. The programmable dividers operate under the control of data, clock and enable commands. The prime second local oscillator 2LO produces a fixed frequency signal at 1350 MHz, and employs a feedback path to ensure that the frequency is maintained. By these means the required frequencies 225 and 135 MHz can be derived from the 13.0 MHz reference, as can the required first local oscillator frequencies.

The synthesised first local oscillator accomplishes operation for PCS1900 transmit and receive together with ODYSSEY transmit by having a local oscillator which tunes a single band 1705 to 1775 MHz. In the case of PCS1900 the 1LO tunes in 200 kHz spaced channels, defined by the by R/Q divider on the reference to the phase lock loop, and the programmable divider. For the ODYSSEY receive mode a separate 1LO synthesiser is used tuning 2258.5 to 2275.0 MHz.

Referring now to Figure 8, there is shown one embodiment of the present invention, comprising a dual mode radio front end for the reception of both digital cellular DCS1800 signals and digital cordless DECT signals. DCS1800 operates in the frequency band 1805 to 1880 MHz on the receive downlink to the handset and in the 1710 to 1785 MHz band on the transmit uplink from the handset. DECT operates in the frequency 1881.792 to 1897.344 Mhz and in the same frequency channel on the handset transmit and receive links by way of time division duplexing.

The DCS1800 handset operates either in a transmit mode or in a receive mode and uses frequency division duplex: DECT also operates in transmit or receive mode but uses the same frequency channel with time division duplex. For this reason the switch 14 from the antenna 12 has three positions. Band pass filters 16 and low pass filter 18 are provided on the input and output lines for the DCS1800 signals whilst a second switch 306 and single RF band filter 20 are employed for DECT signals. Low noise amplifiers LNA1 and LNA2 are provided on the input respectively. Power amplification is provided by a dual band PA.

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For DCS1800 and DECT separate band filters 22 and 24 are provided from the outputs of the LNAs to the inputs to the mixers 30 and 32. Following the buffer amplifiers 36 and 38 separate IF channel filters are provided 40 and 42. Preferably these filters are radio frequency surface acoustic wave SAW devices. The advantages provided are such that the separate receive bands can be converted down to an IF and separate channel selection performed, so that over each RF band a large number of channels can be determined .

10

Turning now to the receive path for the digital DCS1800 signals, when the switch 14 directs incoming digital DCS1800 signals to the DCS1800 receive path, the signals from the band select filter 22 are passed to a mixer 30 which mixes the received signal with a signal from a synthesised local oscillator 34 to produce an intermediate frequency (IF) signal at 225 MHz which is subsequently amplified by further amplifying means 36. The DCS1800 signals are then filtered by the 225 MHz GSM IF channel filter (the DCS1800 modulation protocol is the same as that of GSM so the filter is a GSM channel filter), and the filtered IF signal is then passed through a second switching circuit 44 which operates simultaneously with the first switch 14 by a mode control means (not shown). The mode control means identifies whether the signals are DCS1800 or DECT modulation and determines in which mode the transceiver is operating.

25

The receive signal output from switch 44 is fed to an IF amplifier with automatic gain control and a receive signal strength indicator (RSSI) 48. After amplification, the signal path is routed through splitter 50 and the signals are output to a mixer pair 52 and 54, and after mixing with a quadrature 225 MHz signal derived through switchable division in 56 from a synthesised 450 MHz second local oscillator (2LO), in-phase and quadrature baseband signals are amplified by amplifiers 60 and 62, to provide output signals at 64 and 66 to be fed to the analogue to digital converters and digital signal processing means (not shown).

35

If a DECT radio signal were present at the antenna and a decision made to receive that signal, the switch 14 would feed the signal from the

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antenna 12 through filter 20, amplifier LNA2, filter 24 to mixer 32. At mixer 32 the radio frequency signal is downconverted, using a synthesised local oscillator to the intermediate frequency (IF) of 110.592 MHz which is different from that of the DCS1800 case. This IF signal is subsequently amplified by amplifying means 38 before channel selection by the 110.592 MHz filter means 42. As in the case with the DCS1800 signal, the DECT IF signal is output through the common IF amplifier, which, after amplification and mixing with a quadrature local oscillator signal of 110.592 MHz derived through divider 56 from the dual frequency second local oscillator (2LO), in-phase and quadrature baseband signals are provided at 64 and 66 to be fed to the analogue to digital converters and digital signal processing means (not shown).

The custom receive IC uses separate DCS1800 and DECT LNA/down converters with external SAW image filters. Separate SAW intermediate frequency filter at 225 and 110.592 MHz are used for DCS1800 and DECT. For both systems the IF filtering is arranged to be for a single channel.

For transmit, the DCS1800 and DECT baseband signals are raised to intermediate frequencies (IFs) at 130 and 110.592 MHz within a phase lock loop (PLL) which effectively converts the IF to any selected channel in either the DCS1800 or DECT RF bands. The baseband signals, derived from digital signal processing and digital to analogue converters (not shown), are input at ports 70 and 72, and are amplified by amplifiers 74 and 76 prior to upconversion to the phase lock loop IF of 130 or 110.592 MHz in the quadrature modulator combination of mixers 82 and 84. The upconverted modulation at the IF containing either the DCS1800 or DECT signals is filtered 86 and applied to the divider 88 prior to being fed to the PLL phase detector. The reference input to the phase detector is derived by a divider 90 from the switched 108 dual reference oscillators (not shown). A charge pump 94 feeds from the phase detector to the loop filter 96, whose output controls the dual-band VCO 98. The phase lock loop is closed and effectively channel tuned by filtering 26 a sample of the preamp 100 output, and downconverting to the 130 or 110.592 MHz IF via the mixer 68 using the same synthesised first local oscillator 34 as for receive. This IF is filtered 28 and applied in the loop to

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the modulator at the 90 degree phase shifter 80. The transmit PLL intermediate frequency filters will not need to be of high shape factor and can be arranged to bracket both IFs.

- 5 In terms of the frequency generation or local oscillator requirements for this dual mode radio, it can be seen from Figures 8 and 9 that all the required local oscillator frequencies are derived from two voltage controlled oscillators, referenced via two phase lock loops to 13.0 or 13.824 MHz.

10

- With reference to the radio frequency plan of Figure 9, it can be seen that the first local oscillator signals are derived from a single dual band voltage controlled oscillator (VCO). Band selection operation of the dual band first local oscillator VCO in the frequency band 1580 to 1655 MHz for the DCS1800 case or in the frequency band 1771.200 to 1786.752 MHz for the DECT case is used and four intermediate frequencies (IFs) result. The two receive IFs have been arranged to be integer related to the 450/442.368 MHz dual prime second local oscillator (2LO) and to be at frequencies convenient for SAW channel filter implementation. The two transmit IFs have been arranged to be at frequencies reducible by integer division to a 13 or 13.824 MHz reference frequency.

- The quadrature signal local oscillators required to mix the two receive IFs down to baseband are readily derived by division from the 450/442.368 MHz 2LO. The dual frequency prime 2LO is switched to the required frequency for DCS1800 or DECT and respectively referenced to 13.0 or 13.824 MHz. For the DCS1800 receive state the 1805 to 1880 MHz band is downconverted to a 225 MHz IF using a channel tuning synthesised 1LO covering the range 1580 to 1655 MHz. The channel filtered 225 MHz IF is downconverted to baseband in-phase and quadrature signals using a 225 MHz quadrature local oscillator derived from the dual frequency 2LO when programmed for the 450 MHz DCS1800 mode.

- 35 For the DCS1800 transmit state, in-phase and quadrature baseband signals are upconverted to a 130 MHz signal using the 130 MHz IF derived within the phase lock loop by downconversion with the channel

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tuning receive local oscillator from the RF output. The 130 MHz IF is divided and phase locked to a 13 MHz reference and this output controls the transmit RF VCO frequency via a charge pump and loop filter.

- 5 For the DECT receive state the 1881.792 to 1897.344 MHz band is downconverted to a 110.592 MHz IF using a channel tuning synthesised 1LO covering the range 1771.200 to 1786.752 MHz. The channel filtered 110.592 MHz IF is downconverted to baseband in-phase and quadrature signals using a 110.592 MHz quadrature local oscillator oscillator
10 derived from the dual frequency 2LO when programmed for the 442.368 MHz DECT mode.

- The DECT transmit state is similar to the DCS1800 in that in-phase and quadrature baseband signals are upconverted to a 110.592 MHz signal
15 using a 110.592 MHz IF derived within the phase lock loop by downconversion with the channel tuning receive local oscillator from the RF output. The 110.592 MHz IF is divided and phase locked to a 13.824 MHz reference and this output controls the transmit RF VCO frequency via a charge pump and loop filter.

- 20 A feature of the 1LO synthesised frequency generation is that in the DCS1800 mode the transmit and receive local oscillator tuning bands are arranged to exactly overlap by using different transmit and receive IFs. In this way the tuning range of the 1LO can be constrained such that
25 retuning the 1LO between transmit and receive is unnecessary. It is a requirement in the DECT mode for the transmit and receive first local oscillators to tune the same frequency range and channel, as the DECT radio uses the same air frequency for uplink and downlink. The 1LO is arranged to have two frequency bands at 1771.200 to 1786.752 MHz to
30 cover the requirements of DECT and 1580 to 1655 MHz to cover DCS1800.

- The two receive quadrature second local oscillator signals are derived from the prime 2LO as follows. The prime second local oscillator
35 provides a synthesised 450 or 442.368 MHz signal 58 which is fed to a switchable divide by two or four divider 56 resulting in a 225 or 110.592 MHz local oscillator. For the DCS1800 transmit path the 13 MHz

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reference is divided by 1 in divider 90 resulting in the 13 MHz reference. For the DECT transmit path the 13.824 MHz is divided by 1 in divider 90 resulting in the 13.824 MHz reference (the divider will already be a part of the integrated circuit and can be set to divide by 1).

5

Referring now to the local oscillators, they receive a 13.0 or 13.824 MHz signal which is processed through dividers set by control signals: this allows the local oscillators to operate in the DCS1800 or the DECT modes. Associated with the dual band first local oscillator, there are
10 band control means operable to adjust the local oscillator frequency in response to channel detection means which detect the channel employed in any communication. In the case of the mobile initiating a call then the channel detection means will be able to detect which channels are available and be able to effect the control switches for the
15 DCS1800-DECT circuits as appropriate. The programmable dividers operate under the control of data, clock and enable commands. The prime second local oscillator 2LO produces a two frequencies at 450 and 442.368 MHz, and employs a feedback path to ensure that the frequency is maintained. By these means the required frequencies 225, 130, and
20 110.592 MHz can be derived from the 13.0 and 13.824 MHz references, as can the required first local oscillator frequencies.

The synthesised first local oscillator accomplishes dual mode operation by having a dual band local oscillator which tunes dual bands; 1580
25 to 1655 MHz for DCS1800 or 1771.200 to 1786.752 MHz for DECT. In the DCS1800 mode the 1LO tunes in 200 kHz spaced channels and in the DECT mode 1.728 MHz spaced channels, defined by the by R/Q divider on the reference to the phase lock loop, and the programmable divider.

30

Referring now to Figure 10, there is shown one embodiment of the present invention, comprising a dual mode radio front end for the reception of both digital DCS1800 signals and GSM signals. GSM operates in the frequency band 925 to 960 MHz on the receive link from
35 the basestation to the handset and in the 880 to 915 MHz band on the transmit link from the handset to the basestation.

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The DCS1800 and GSM handsets both operate in either a transmit mode or in a receive mode. For this reason the switch 14 from the antenna 12 has four positions. Band pass filters 16,18 are provided on the input and output lines for the DCS1800 signals along with similar
5 filters 20 and 406 for GSM. Low noise amplifiers LNA are provided on the input and output lines respectively. Power amplification is provided by a dual band power amplifier PA.

For DCS1800 and GSM separate band filters 22 and 24 are provided
10 from the outputs of the LNAs to the inputs to the mixers 30 and 32. After buffer amplifiers 36 and 38 the different system signals are applied to a switch which selects either signal to apply to the common channel IF filter 40. Preferably this filter is a radio frequency surface acoustic wave SAW device. The advantages provided are such that the separate receive
15 bands can be converted down to an IF and separate channel selection performed, so that over the each RF band a large number of channels can be determined .

Turning now to the receive path for the digital DCS1800 signals, when
20 the switch 414 directs incoming digital DCS1800 signals to the DCS1800 receive path through the band filter 16 and to the LNA1 and band filter 22, the signals from the band select filter 22 are passed to a mixer 30 which mixes the received signal with a signal from a synthesised local oscillator 34 to produce an intermediate frequency (IF)
25 signal at 225 MHz which is subsequently amplified by further amplifying means 36. The DCS1800 signals are then selected by switch 44 which operates simultaneously with the first switch 14 by a mode control means (not shown). The mode control means identifies whether the signals are DCS1800 or GSM modulation and determines in which
30 mode the transceiver is operating. The signal is then filtered by the 225 MHz IF channel filter.

The receive signal output from IF filter 40 is fed to an IF amplifier with automatic gain control and a receive signal strength indicator (RSSI) 48.
35 After amplification, the signal path is routed through splitter 50 and the signals are output to a mixer pair 52 and 54, and after mixing with a quadrature 225 MHz signal derived through division in 56 from a

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synthesised 450 MHz second local oscillator (2LO), in-phase and quadrature baseband signals are amplified by amplifiers 60 and 62, to provide output signals at 64 and 66 to be fed to the analogue to digital converters and digital signal processing means (not shown).

5

If GSM radio signal were present at the antenna and a decision made to receive that signal, the switch 14 would feed the signal from the antenna 12 through filter 20, amplifier LNA2, filter 24 to mixer 32. At mixer 32 the radio frequency signal is downconverted, using a synthesised local oscillator to the same intermediate frequency (IF) of 225 MHz as that of the DCS1800 case. This IF signal is subsequently amplified by amplifying means 38 before being selected by switch 44 and applied to the channel selection filter 40. As in the case with the DCS1800 signal, the GSM IF signal is output through the common IF amplifier, which, after amplification and mixing with a quadrature 225 MHz local oscillator signal derived through divider 56 from the second local oscillator (2LO), in-phase and quadrature baseband signals are provided at 64 and 66 to be fed to the analogue to digital converters and digital signal processing means (not shown).

20

The custom receive IC uses separate DCS1800 and GSM LNA/down converters with external SAW image filters. A common SAW intermediate frequency filter at 225 MHz is used for DCS1800 and GSM. The IF filtering is arranged to be for a single channel.

25

For transmit, the DCS1800 and GSM baseband signals are raised to intermediate frequencies (IFs) at 135 and 270 MHz within a phase lock loop (PLL) which effectively converts the IF to any selected channel in either the DCS1800 or GSM RF bands. The baseband signals, derived from digital signal processing and digital to analogue converters (not shown), are input at ports 70 and 72, and are amplified by amplifiers 74 and 76 prior to upconversion to the phase lock loop IF of 135 or 270 MHz in the quadrature modulator combination of mixers 82 and 84. The upconverted modulation at the IF containing either the DCS1800 or GSM signals is filtered 86 and applied to the divider 88 prior to being fed to the PLL phase detector. The reference input to the phase detector is derived by a divider 90 from the prime second local oscillator 58. A charge pump

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- 94 feeds from the phase detector to the loop filter 96, whose output controls the dual-band VCO 98. The phase lock loop is closed and effectively channel tuned by filtering 26 a sample of the preamp 100 output, and downconverting to the 135 or 270 MHz IF via the mixer 68
- 5 using the same synthesised first local oscillator 34 as for receive. This IF is filtered 28 and applied in the loop to the modulator at the 90 degree phase shifter 80. The transmit PLL intermediate frequency filters will not need to be of high shape factor and can be arranged to bracket both IFs.
- 10 In terms of the frequency generation or local oscillator requirements for this dual mode radio, it can be seen from Figures 10 and 11 that all the required local oscillator frequencies are derived from two voltage controlled oscillators, referenced via two phase lock loops to 13.0 MHz.
- 15 With reference to the radio frequency plan of Figure 11, it can be seen that the first local oscillator signals are derived from a single dual band voltage controlled oscillator (VCO). Band selection operation of the dual band first local oscillator VCO in the frequency band 1575 to 1650 MHz for the DCS1800 case or in the frequency band 1150 to 1185 MHz for
- 20 the GSM case is used and three intermediate frequencies (IFs) result. The two receive IFs have been arranged to be integer related to the 450 MHz prime second local oscillator (2LO) and to be at frequencies convenient for SAW channel filter implementation. The two transmit IFs have been arranged to be at frequencies reducible by integer division to
- 25 a 45 MHz reference frequency.
- The quadrature signal local oscillators required to mix the two receive IFs down to baseband are readily derived by division from the 450 MHz 2LO. For the DCS1800 receive state the 1805 to 1880 MHz band is
- 30 downconverted to a 225 MHz IF using a channel tuning synthesised 1LO covering the range 1575 to 1655 MHz. The channel filtered 225 MHz IF is downconverted to baseband in-phase and quadrature signals using a 225 MHz quadrature local oscillator derived from the 450 MHz 2LO.
- 35 For the DCS1800 transmit state, in-phase and quadrature baseband signals are upconverted to a 135 MHz signal using the 135 MHz IF derived within the phase lock loop by downconversion with the channel

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tuning receive local oscillator from the RF output. The 135 MHz IF is divided and phase locked to a 45 MHz reference and this output controls the transmit RF VCO frequency via a charge pump and loop filter.

- 5 For the GSM receive state the 925 to 960 MHz band is downconverted to a 225 MHz IF using a channel tuning synthesised 1LO covering the range 1150 to 1185 MHz. The channel filtered 225 MHz IF is downconverted to baseband in-phase and quadrature signals using a 225 MHz quadrature local oscillator derived from the 450 MHz 2LO.

10

- The GSM transmit state is similar to the DCS1800 in that in-phase and quadrature baseband signals are upconverted to a 270 MHz signal using a 270 MHz IF derived within the phase lock loop by downconversion with the channel tuning receive local oscillator from the
15 RF output. The 270 MHz IF is divided and phase locked to a 45 MHz reference and this output controls the transmit RF VCO frequency via a charge pump and loop filter.

- A feature of the 1LO synthesised frequency generation is that the
20 transmit and receive local oscillator tuning bands are arranged to overlap by using different transmit and receive IFs. In this way the overall tuning range of the 1LO for DCS1800 can be constrained such that transmit to receive switching can be achieved in an allowable time. In the GSM case it has been arranged that the transmit and receive first
25 local oscillators exactly overlap.

- The two receive quadrature second local oscillator signals are derived from the prime 450 MHz 2LO as follows. The prime second local oscillator provides a synthesised 450 MHz signal which is fed to a
30 switchable divide by two divider 56 resulting in a 225 MHz local oscillator. For the transmit path the 450 MHz is divided by 10 in divider 90 resulting in the 45 MHz reference.

- Referring now to the local oscillators, they receive a 13 MHz signal which
35 is processed through dividers set by control signals: this allows the local oscillators to operate in the DCS1800 Transmit and receive mode, or the GSM mode. Associated with the dual band first local oscillator, there are

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band control means operable to adjust the local oscillator frequency in response to channel detection means which detect the channel employed in any communication. In the case of the mobile initiating a call then the channel detection means will be able to detect which channels are available and be able to effect the control switches for the DCS1800-GSM circuits as appropriate. The programmable dividers operate under the control of data, clock and enable commands. The prime second local oscillator 2LO produces a fixed frequency signal at 450 MHz, and employs a feedback path to ensure that the frequency is maintained. By these means the required frequencies 225, 270, 135 and 45 MHz can be derived from the 13 MHz reference, as can the required first local oscillator frequencies.

The synthesiser first local oscillator accomplishes dual mode operation by having a dual band local oscillator which tunes a single band for transmit and receive either 1575 to 1655 MHz for the DCS1800 or 1150 to 1185 MHz for GSM transmit and receive. In both case of DCS1800 and GSM the 1LO tunes in 200 kHz spaced channels, defined by the by R/Q divider on the reference to the phase lock loop, and the programmable divider.

Referring now to Figure 12, there is shown one embodiment of the present invention, comprising a dual mode radio front end for the reception of both digital cellular GSM signals and digital cordless DECT signals. DECT operates in the frequency 1881.792 to 1897.344 MHz and in the same frequency channel on the handset transmit and receive links by way of time division duplexing.

The GSM handset operates either in a transmit mode or in a receive mode and uses frequency division duplex: DECT also operates in transmit or receive mode but uses the same frequency channel with time division duplex. For this reason the switch 14 from the antenna 12 has three positions. Band pass filters 16 and low pass filter 18 are provided on the input and output lines for the GSM signals whilst a second switch 506 and single RF band filter 20 are employed for DECT signals. Low noise amplifiers LNA1 and LNA2 are provided on the input respectively. Power amplification is provided by a dual band PA.

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For GSM and DECT separate band filters 22 and 24 are provided from the outputs of the LNAs to the inputs to the mixers 30 and 32. Following the buffer amplifiers 36 and 38 separate IF channel filters are provided
5 40 and 42. Preferably these filters are radio frequency surface acoustic wave SAW devices. The advantages provided are such that the separate receive bands can be converted down to an IF and separate channel selection performed, so that over each RF band a large number of channels can be determined .

10

Turning now to the receive path for the digital GSM signals, when the switch 14 directs incoming digital GSM signals to the GSM receive path, the signals from the band select filter 22 are passed to a mixer 30 which mixes the received signal with a signal from a synthesised local oscillator
15 34 to produce an intermediate frequency (IF) signal at 225 MHz which is subsequently amplified by further amplifying means 36. The GSM signals are then filtered by the 225 MHz IF channel filter, and the filtered IF signal is then passed through a second switching circuit 44 which operates simultaneously with the first switch 14 by a mode control
20 means (not shown). The mode control means identifies whether the signals are GSM or DECT modulation and determines in which mode the transceiver is operating.

The receive signal output from switch 44 is fed to an IF amplifier with automatic gain control and a receive signal strength indicator (RSSI) 48.
25 After amplification, the signal path is routed through splitter 50 and the signals are output to a mixer pair 52 and 54, and after mixing with a quadrature 225 MHz signal derived through switchable division in 56 from a synthesised 450 MHz second local oscillator (2LO), in-phase and
30 quadrature baseband signals are amplified by amplifiers 60 and 62, to provide output signals at 64 and 66 to be fed to the analogue to digital converters and digital signal processing means (not shown).

If a DECT radio signal were present at the antenna and a decision made
35 to receive that signal, the switch 14 would feed the signal from the antenna 12 through filter 20, amplifier LNA2, filter 24 to mixer 32. At mixer 32 the radio frequency signal is downconverted, using a

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synthesised local oscillator to the intermediate frequency (IF) of 110.592 MHz which is different from that of the GSM case. This IF signal is subsequently amplified by amplifying means 38 before channel selection by the 110.592 MHz filter means 42. As in the case with the GSM signal, the DECT IF signal is output through the common IF amplifier, which, after amplification and mixing with a quadrature local oscillator signal 110.592 MHz derived through divider 56 from the dual frequency second local oscillator (2LO), in-phase and quadrature baseband signals are provided at 64 and 66 to be fed to the analogue to digital converters and digital signal processing means (not shown).

The custom receive IC uses separate GSM and DECT LNA/down converters with external SAW image filters. Separate SAW intermediate frequency filter at 225 and 110.592 MHz are used for GSM and DECT. For both systems the IF filtering is arranged to be for a single channel.

For transmit mode, the GSM and DECT baseband signals are raised to intermediate frequencies (IFs) at 270 and 110.592 MHz within a phase lock loop (PLL) which effectively converts the IF to any selected channel in either the GSM or DECT RF bands. The baseband signals, derived from digital signal processing and digital to analogue converters (not shown), are input at ports 70 and 72, and are amplified by amplifiers 74 and 76 prior to upconversion to the phase lock loop IF of 270 or 110.592 MHz in the quadrature modulator combination of mixers 82 and 84. The upconverted modulation at the IF containing either the GSM or DECT signals is filtered 86 and applied to the divider 88 prior to being fed to the PLL phase detector. The reference input to the phase detector is derived by a divider 90 from the dual frequency prime second local oscillator 58. A charge pump 94 feeds from the phase detector to the loop filter 96, whose output controls the dual-band VCO 98. The phase lock loop is closed and effectively channel tuned by filtering 26 a sample of the preamp 100 output, and downconverting to the 270 or 110.592 MHz IF via the mixer 68 using the same synthesised first local oscillator 34 as for receive. This IF is filtered 28 and applied in the loop to the modulator at the 90 degree phase shifter 80. The transmit PLL intermediate frequency filters will not need to be of high shape factor and can be arranged to bracket both IFs.

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In terms of the frequency generation or local oscillator requirements for this dual mode radio, it can be seen from Figures 12 and 13 that all the required local oscillator frequencies are derived from two voltage controlled oscillators, referenced via two phase lock loops to 13.0 or 13.824 MHz.

With reference to the radio frequency plan of Figure 13, it can be seen that the first local oscillator signals are derived from a single dual band voltage controlled oscillator (VCO). Band selection operation of the dual band first local oscillator VCO in the frequency band 1150 to 1185 MHz for the GSM case or in the frequency band 1771.200 to 1786.752 MHz for the DECT case is used and four intermediate frequencies (IFs) result. The two receive IFs have been arranged to be integer related to the 450/442.368 MHz dual prime second local oscillator (2LO) and to be at frequencies convenient for SAW channel filter implementation. The two transmit IFs have been arranged to be at frequencies reducible by integer division to a 45 or 13.824 MHz reference frequency.

The quadrature signal local oscillators required to mix the two receive IFs down to baseband are readily derived by division from the 450/442.368 MHz 2LO. The dual frequency prime 2LO is switched to the required frequency for GSM or DECT and respectively referenced to 13.0 or 13.824 MHz. For the GSM receive state the 925 to 960 MHz band is downconverted to a 225 MHz IF using a channel tuning synthesised 1LO covering the range 1150 to 1185 MHz. The channel filtered 225 MHz IF is downconverted to baseband in-phase and quadrature signals using a 225 MHz quadrature local oscillator derived from the dual frequency 2LO when programmed for the 450 MHz GSM mode.

30

For the GSM transmit state, in-phase and quadrature baseband signals are upconverted to a 270 MHz signal using the 270 MHz IF derived within the phase lock loop by downconversion with the channel tuning receive local oscillator from the RF output. The 270 MHz IF is divided and phase locked to a 45 Mhz reference and this output controls the transmit RF VCO frequency via a charge pump and loop filter.

35

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For the DECT receive state the 1881.792 to 1897.344 MHz band is downconverted to a 110.592 MHz IF using a channel tuning synthesised 1LO covering the range 1771.200 to 1786.752 MHz. The channel filtered 110.592 MHz IF is downconverted to baseband in-phase and quadrature
5 signals using a 110.592 MHz quadrature local oscillator oscillator derived from the dual frequency 2LO when programmed for the 442.368 MHz DECT mode.

The DECT transmit state is similar to the GSM in that in-phase and
10 quadrature baseband signals are upconverted to a 110.592 MHz signal using a 110.592 MHz IF derived within the phase lock loop by downconversion with the channel tuning receive local oscillator from the RF output. The 110.592 MHz IF is divided and phase locked to a 13.824 MHz reference and this output controls the transmit RF VCO frequency
15 via a charge pump and loop filter.

A feature of the 1LO synthesised frequency generation is that in the GSM mode the transmit and receive local oscillator tuning bands are arranged to exactly overlap by using different transmit and receive IFs. In this way
20 the tuning range of the 1LO can be constrained such that retuning the 1LO between transmit and receive is unnecessary. It is a requirement in the DECT mode for the transmit and receive first local oscillators to tune the same frequency range and channel, as the DECT radio uses the same air frequency for uplink and downlink. The 1LO is arranged to have
25 two frequency bands at 1771.200 to 1786.752 MHz to cover the requirements of DECT and 1150 to 1185 MHz to cover GSM.

The two receive quadrature second local oscillator signals are derived from the prime 2LO as follows. The prime second local oscillator
30 provides a synthesised 450 or 442.368 MHz signal which is fed to a switchable divide by two divider 56 resulting in a 225 or 110.592 MHz local oscillator. For the GSM transmit path the 450 MHz is divided by 10 in divider 90 resulting in the 45 MHz reference. For the DECT transmit path the 442.368 MHz is divided by 32 in divider 90 resulting in the
35 13.824 MHz reference.

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Referring now to the local oscillators, they receive a 13.0 or 13.824 MHz signal which is processed through dividers set by control signals: this allows the local oscillators to operate in the GSM or the DECT modes. Associated with the dual band first local oscillator, there are band control means operable to adjust the local oscillator frequency in response to channel detection means which detect the channel employed in any communication. In the case of the mobile initiating a call then the channel detection means will be able to detect which channels are available and be able to effect the control switches for the GSM-DECT circuits as appropriate. The programmable dividers operate under the control of data, clock and enable commands. The prime second local oscillator 2LO produces a two frequencies at 450 and 442.368 MHz, and employs a feedback path to ensure that the frequency is maintained. By these means the required frequencies 225, 270, and 110.592 MHz can be derived from the 13.0 and 13.824 MHz references, as can the required first local oscillator frequencies.

The synthesised first local oscillator accomplishes dual mode operation by having a dual band local oscillator which tunes dual bands; 1150 to 1185 MHz for GSM or 1771.200 to 1786.752 MHz for DECT. In the GSM mode the 1LO tunes in 200 KHz spaced channels and in the DECT mode 1.728 MHz spaced channels, defined by the by R/Q divider on the reference to the phase lock loop, and the programmable divider.

In order to implement a dual mode radio architecture, problems arise in the case of separate frequencies in various circuits. There is a danger that coupling effects giving rise to intermodulation products arise which will distort signals. Thus the case of a single silicon semiconductor substrate is not appropriate because this is a likely case in which intermodulation products will develop. A three chip 'horizontal' partitioning is convenient, with a chip to carry the receive front end circuits; a chip to carry the transmit front end circuits and a chip to carry the two local oscillators. Such a partitioning is necessary whereby the transmit intermediate frequencies do not interfere with the receive path. It is possible to utilise a single chip which is readily available for dual frequency synthesis, and this will reduce spurious clock interference into transmit and receive paths. This minimum complexity three chip solution

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radio architecture keeps the frequency generation simple and easily realisable. Simple low shape factor filtering should suffice for the transmit PLL intermediate frequencies.

- 5 Standard ceramic filters can be used to separate the transmit and receive RF bands of the different operating protocols, with the AMPS and ODYSSEY filters being of the duplexer type. An antenna switch is included to select one of the non-duplexed transmit or receive signals, or the duplexed transmit and receive.

10

Whilst the invention has been described in relation to particular sets of dual frequency/protocols, by suitable choice of intermediate frequencies, the method described can be extended to other dual frequency/protocol scenarios.

15

The disclosures in British patent application No 9603316.2, from which this application claims priority, and in the abstract accompanying this application are incorporated herein by reference.

CLAIMS

1 A radio front end transceiver operable to receive and
transmit radio signals in different frequency bands and modulation
5 formats, the transceiver comprising;

 receive and transmit paths for each modulation format,
wherein common receive and transmit intermediate frequency circuitry is
employed and the local oscillator requirements for the intermediate
frequency to baseband conversion are derived from a single frequency
10 synthesiser.

2 A radio front end transceiver according to claim 1, wherein
the local oscillator requirements for the intermediate frequency to radio
frequency conversion are derived from a single frequency synthesiser.

15

3 A radio front end transceiver according to claim 1 wherein a
single first local oscillator is used and the required dual mode operation
is achieved by using a dual band voltage controlled oscillator and a
programmable synthesiser.

20

4 A radio front end transceiver according to claim 3, wherein
the first local oscillator employs overlap tuning in order to constrain the
voltage controlled oscillator tuning range appropriate for the dual band.

25 5 A radio front end transceiver according to any one of claims
1, 3 or 4, wherein the transceiver comprises a single second local
oscillator is used, wherein the required dual mode operation is achieved
by integer division of the second local oscillator to derive the required
local oscillator input signals.

30

6 A radio front end transceiver according to claim 1 wherein a
two first local oscillators are used and the required dual mode operation
is achieved by employing a single reference frequency source.

35 7 A radio front end transceiver according to any one of claims
1 and 3 to 5, wherein the higher frequency radio system local oscillator

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bands are arranged to overlap, by switching intermediate frequencies, whereby the tuning range is constrained to avoid retuning.

8 A radio front end transceiver according to any one of claims
5 1 to 7, wherein the transceiver is provided with a means to determine the operational mode.

9 A radio transceiver comprising a combination of functional
10 block circuits and frequency plan,
 wherein, in a first circuit, disparate radio air interface signals are
 downconverted, with a first local oscillator, and filtered with switched
 separate intermediate frequency filters, amplified and converted using a
 second local oscillator, to in-phase and quadrature baseband signals, in
15 a common sub-system of functional blocks,
 wherein, in a second circuit, disparate baseband modulation format in-
 phase and quadrature signals are upconverted to their respective
 disparate radio air interface signals in a common subsystem of functional
 blocks configured as a phase lock loop upconverting modulator, which
 uses a common first local oscillator to the receive circuit and a reference
20 derived from the prime second local oscillator,
 wherein, the frequency synthesis is arranged so that only two phase
 locked voltage controlled oscillators are required, with the two receive
 second local oscillator frequencies integer relationship derived from the
 prime second local oscillator, itself phase locked to the frequency
25 reference, and the first local oscillator frequencies all derived from the
 same frequency reference, with the higher frequency radio system local
 oscillator bands arranged to exactly overlap, by switching intermediate
 frequencies, so as to constrain the tuning range to avoid retuning
 between transmit and receive.

30 10 A mobile radfio handset incorporating a radio front end
 transceiver in accordance with any one or more of claims 1-9.

35 11 A method of operating a radio front end transceiver
 operable to receive and transmit radio signals in different frequency
 bands and modulation formats, wherein for each modulation format the
 same receive and transmit intermediate frequency circuitry is employed

-40-

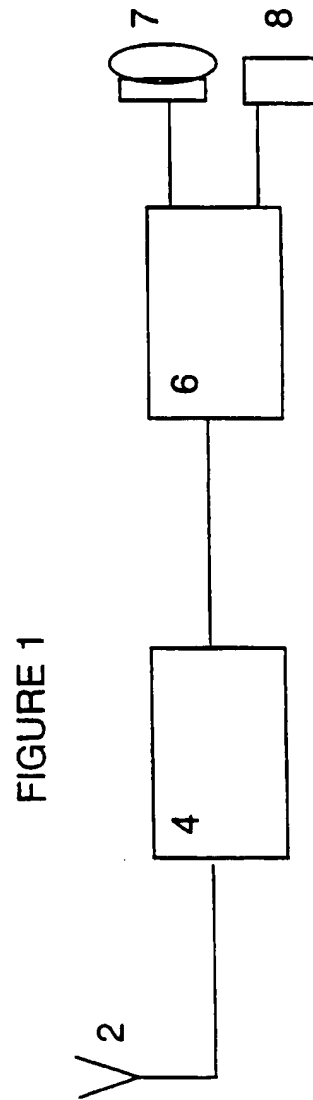
and the local oscillator requirements for the intermediate frequency to baseband conversion are derived from a single frequency synthesiser.

12 A method of operating a radio front end transceiver
5 operable to receive and transmit radio signals in different frequency
bands and modulation formats, wherein for each modulation format the
same receive and transmit circuitry is employed and the local oscillator
requirements for the radio frequency to intermediate frequency and
intermediate frequency to baseband conversions are derived from a
10 single frequency synthesiser.

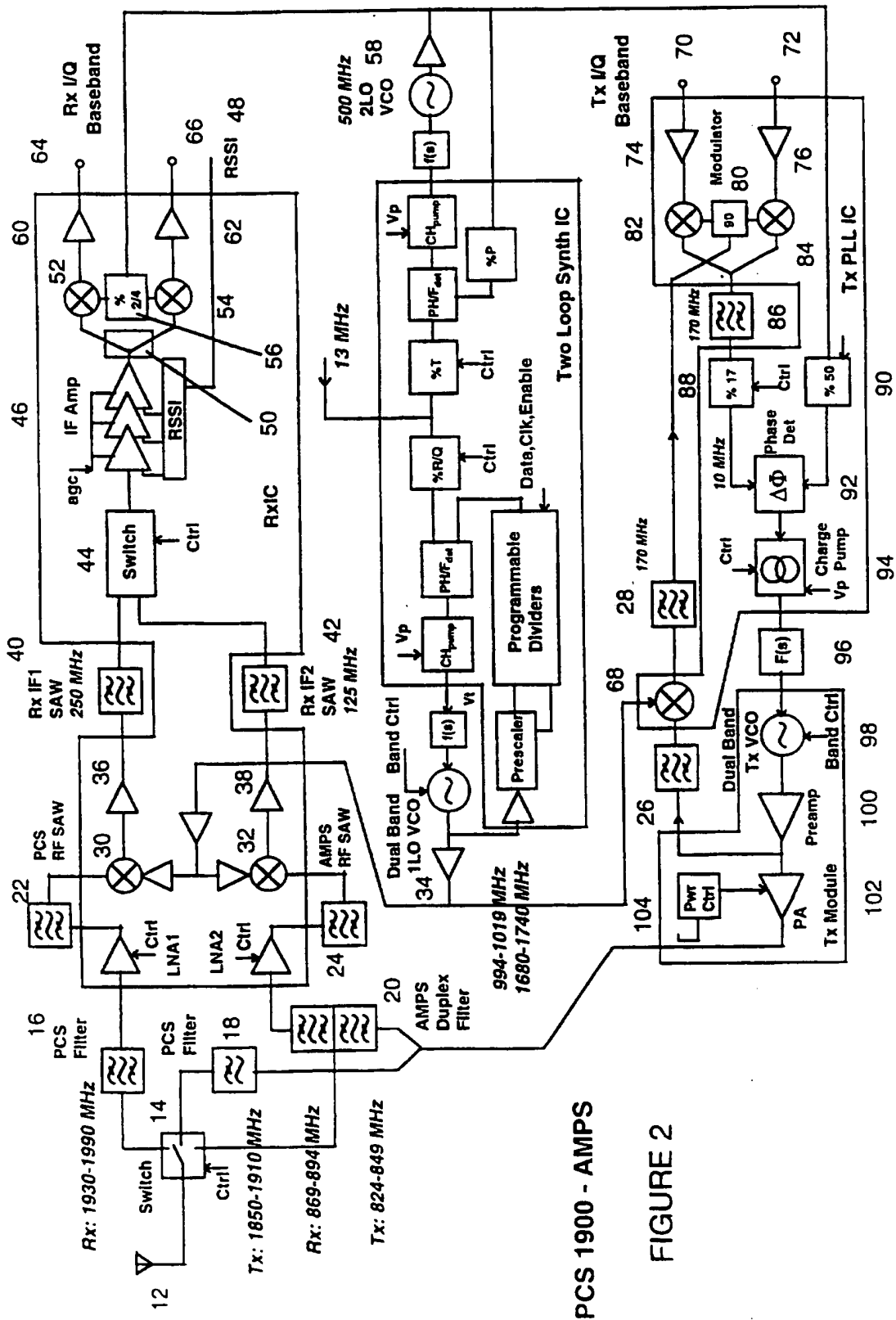
13 A method of operating a mobile radio handset operable to
receive and transmit radio signals in different frequency bands and
modulation formats, wherein for each modulation format the same receive
15 and transmit intermediate frequency circuitry is employed and the local
oscillator requirements for the intermediate frequency to baseband
conversion are derived from a single frequency synthesiser.

14 A method of operating a mobile radio handset operable to
20 receive and transmit radio signals in different frequency bands and
modulation formats, wherein for each modulation format the same receive
and transmit circuitry is employed and the local oscillator requirements
for the radio frequency to intermediate frequency and intermediate
frequency to baseband conversions are derived from a single frequency
25 synthesiser.

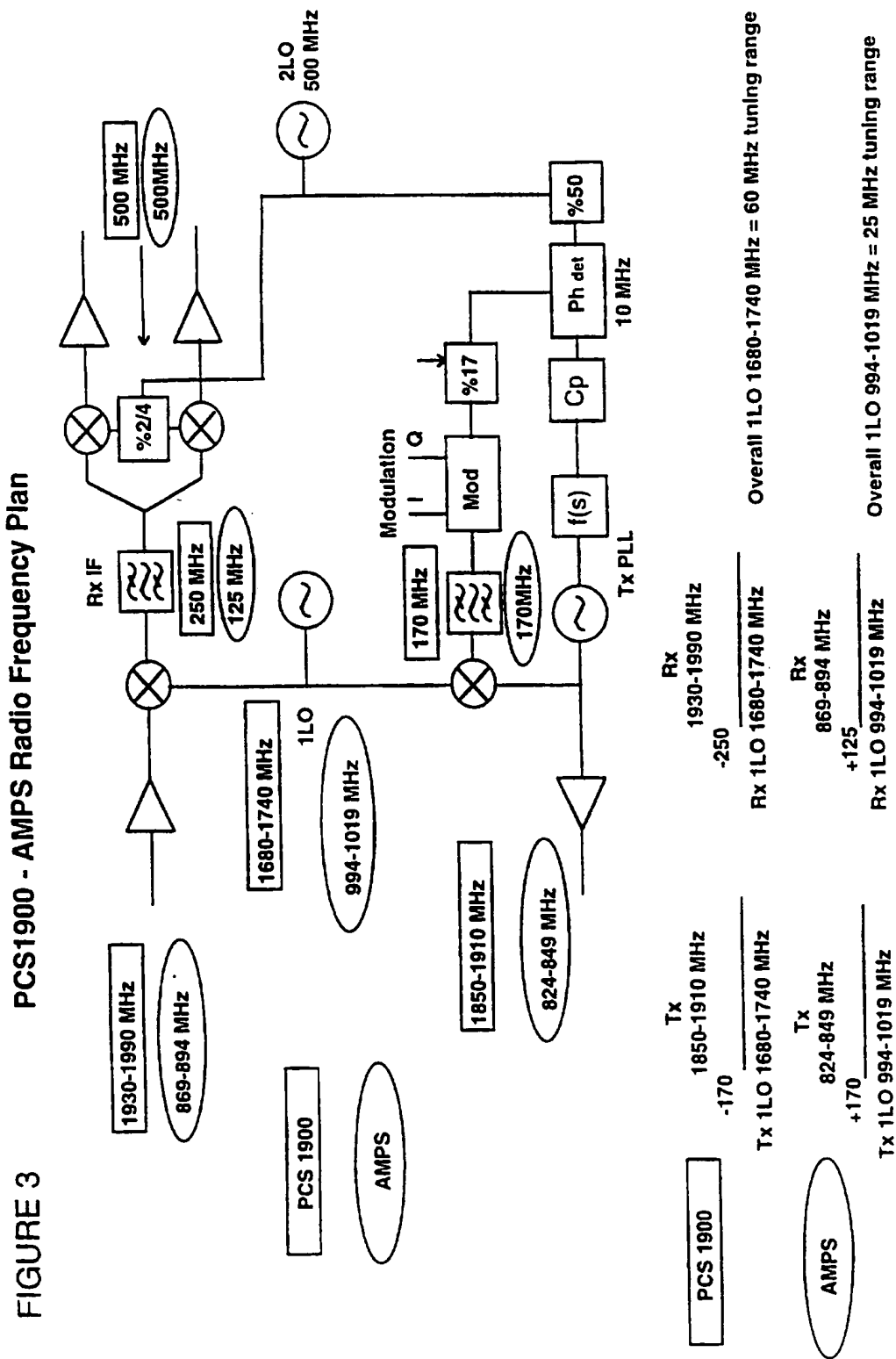
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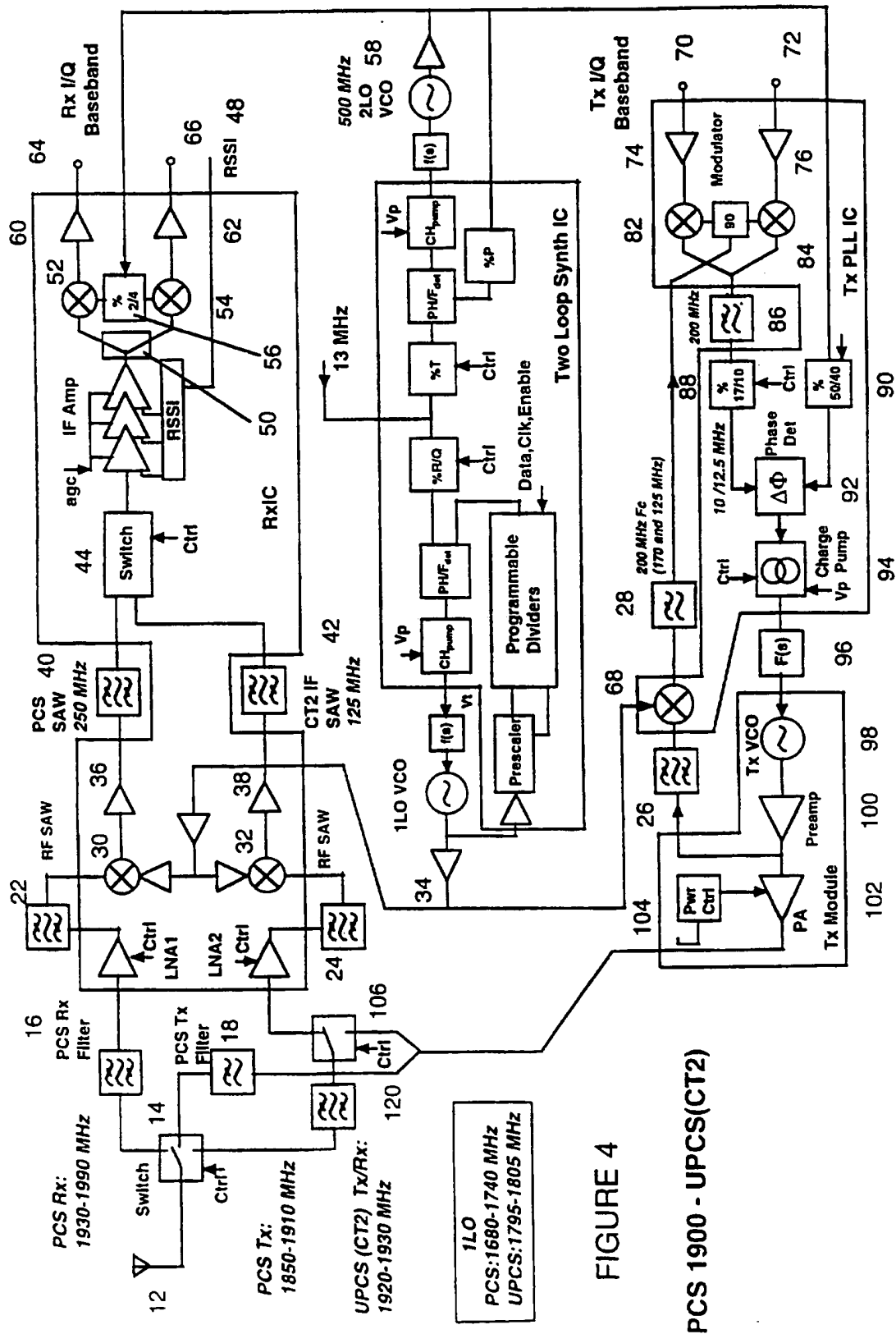
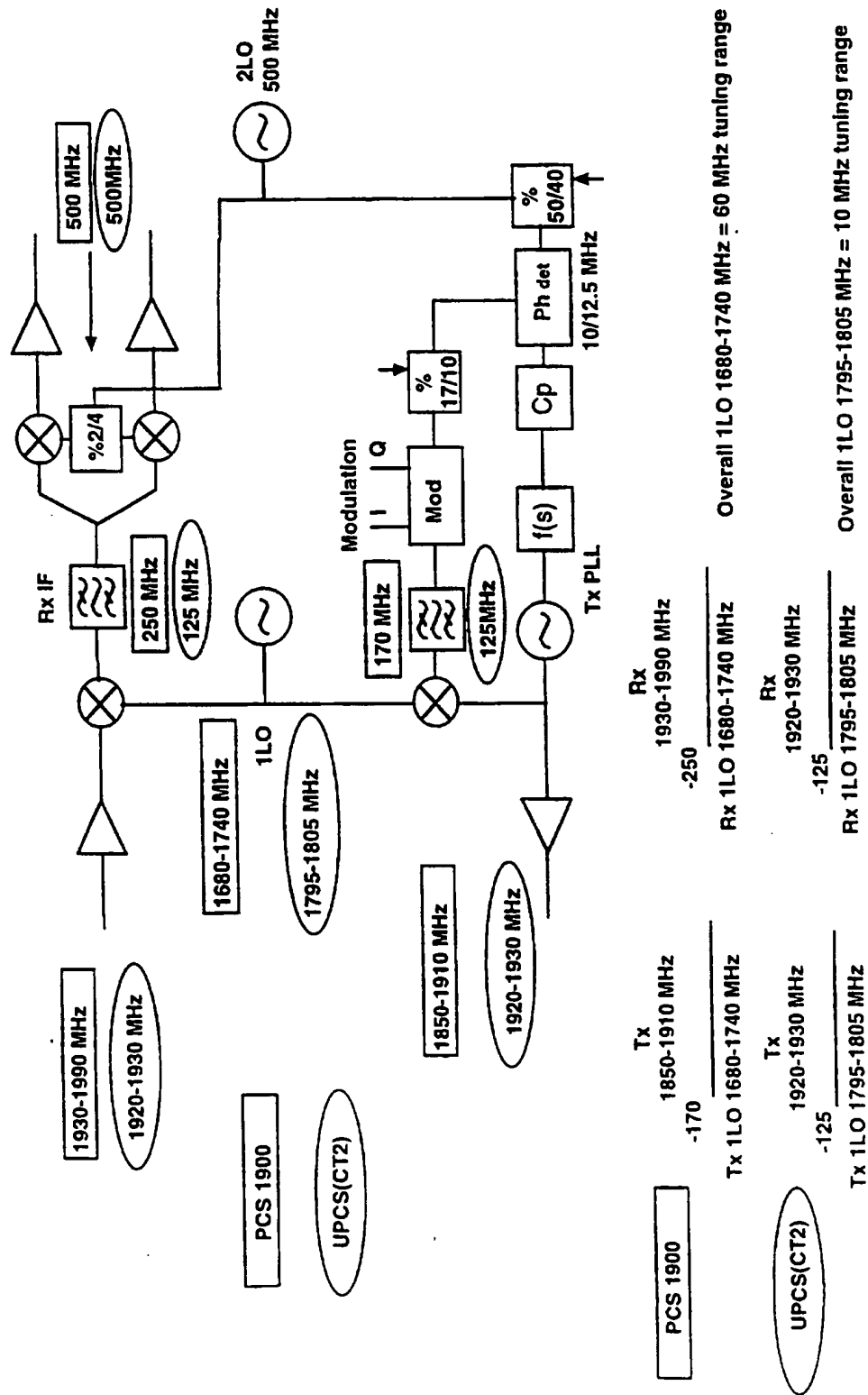


FIGURE 4

PCS 1900 - UPACS(CT2)

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FIGURE 5 PCS1900 - UPCS(CT2)



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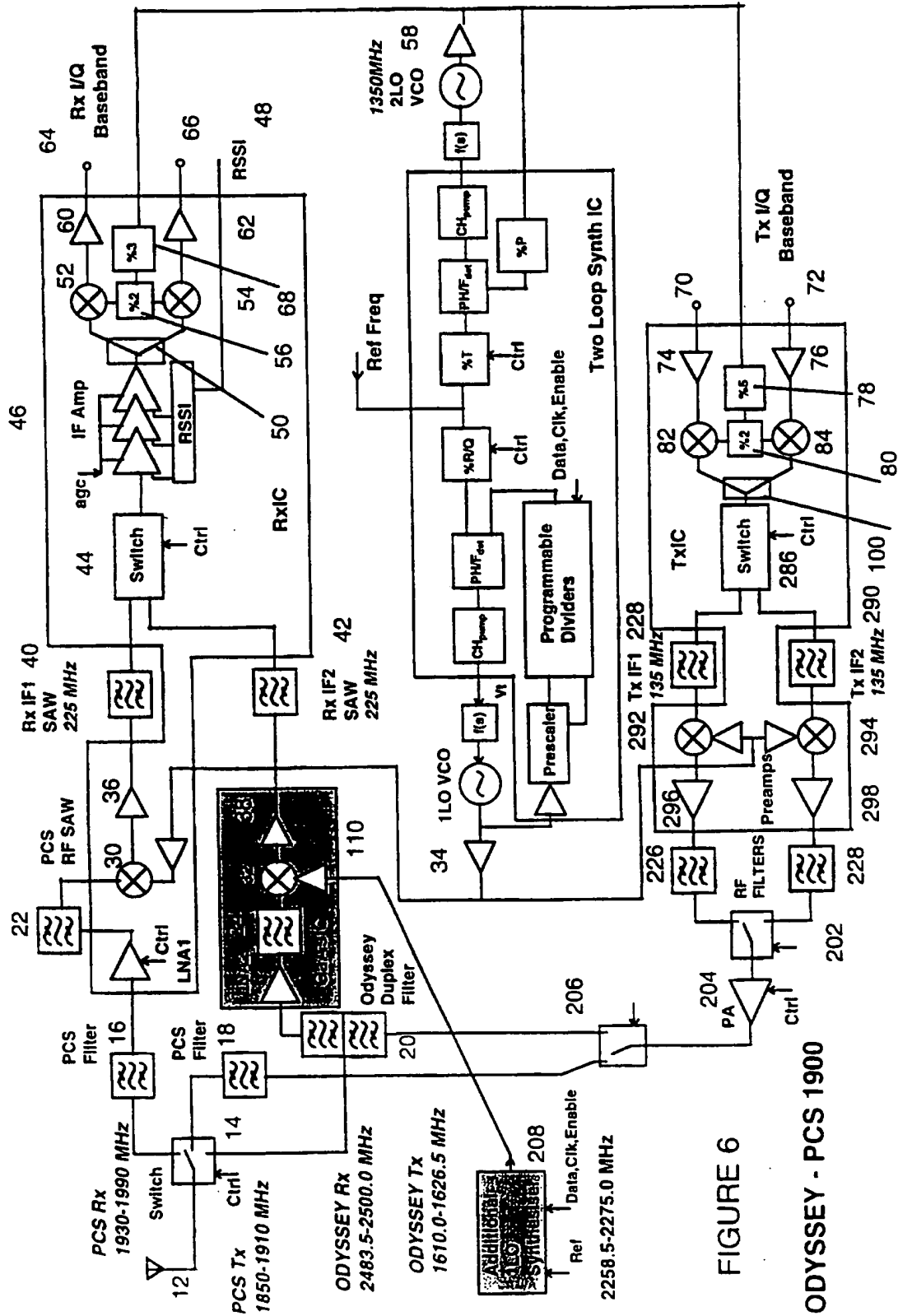
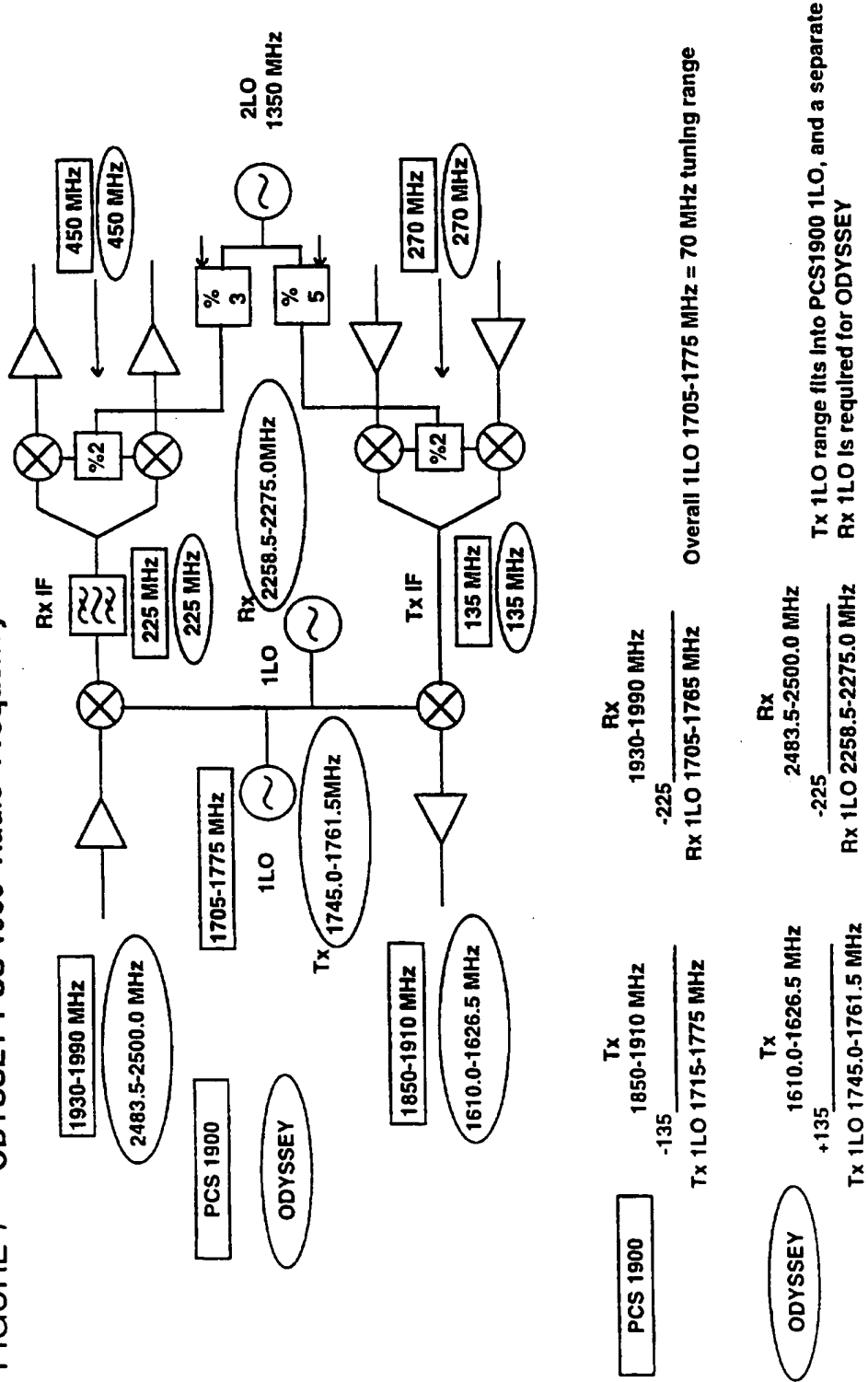


FIGURE 6

ODYSSEY - PCS 1900

FIGURE 7 ODYSSEY-PCS 1900 Radio Frequency Plan



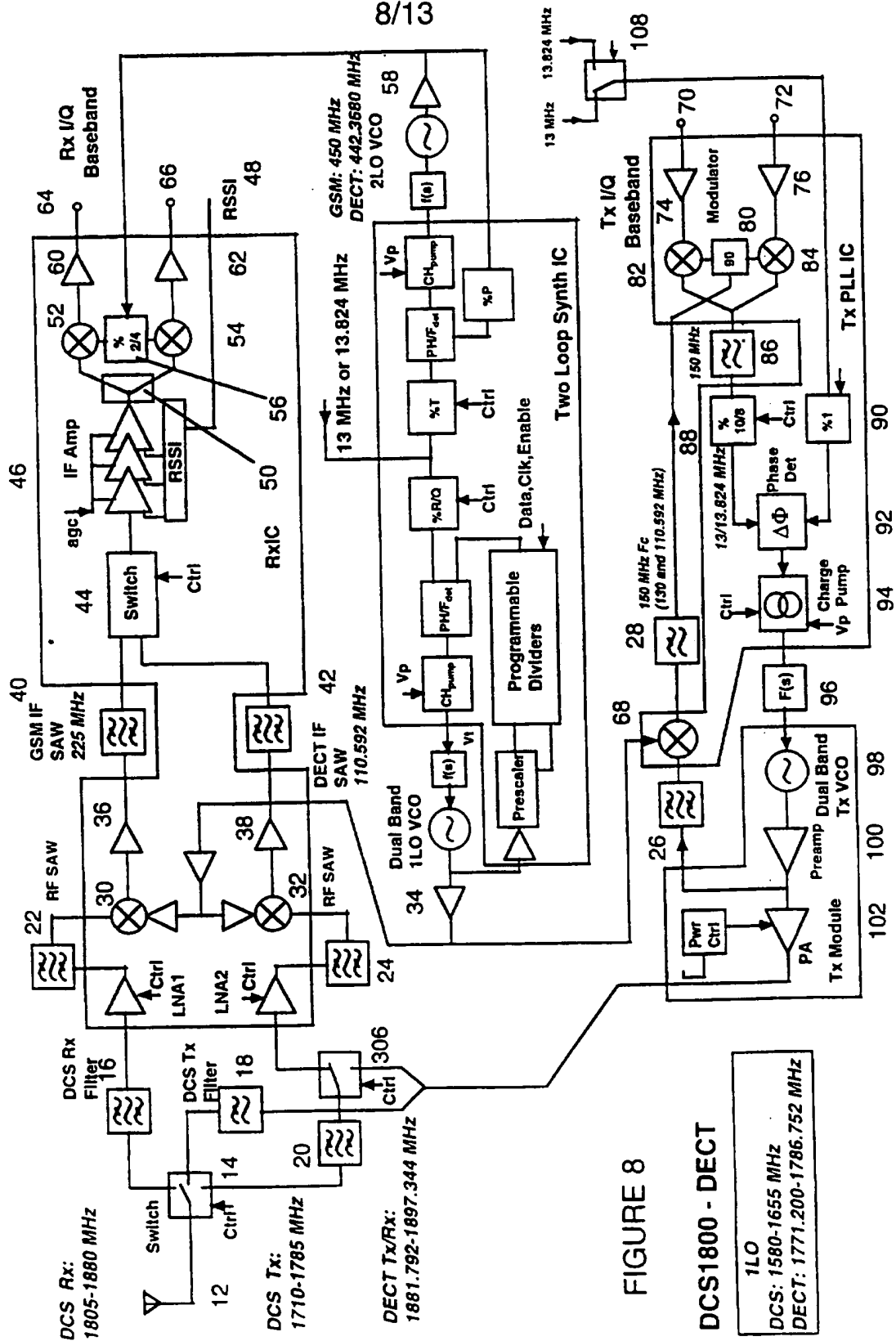


FIGURE 8

DCS1800 - DECT

1LO
DCS: 1580-1655 MHz
DECT: 1771.200-1786.752 MHz

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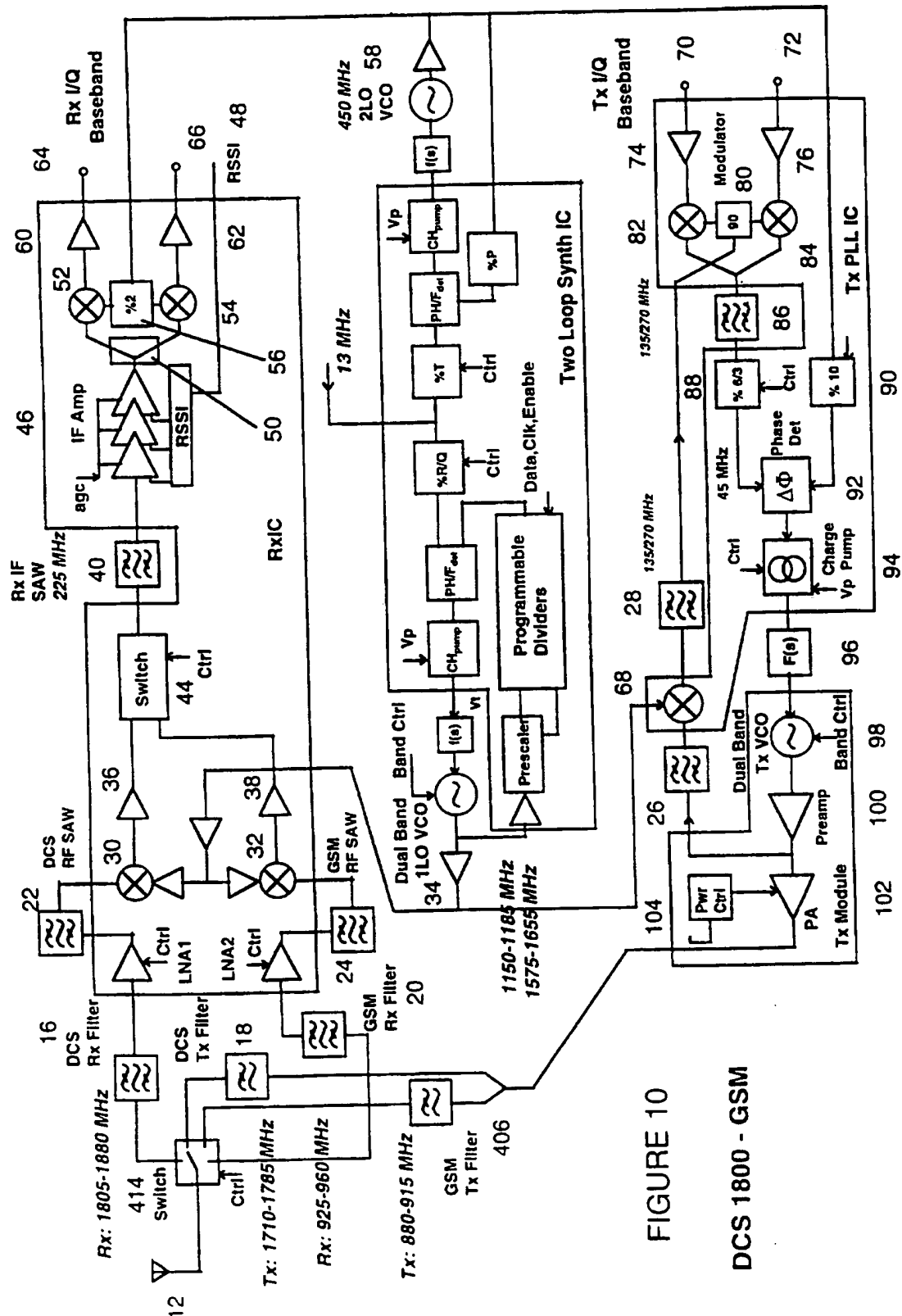
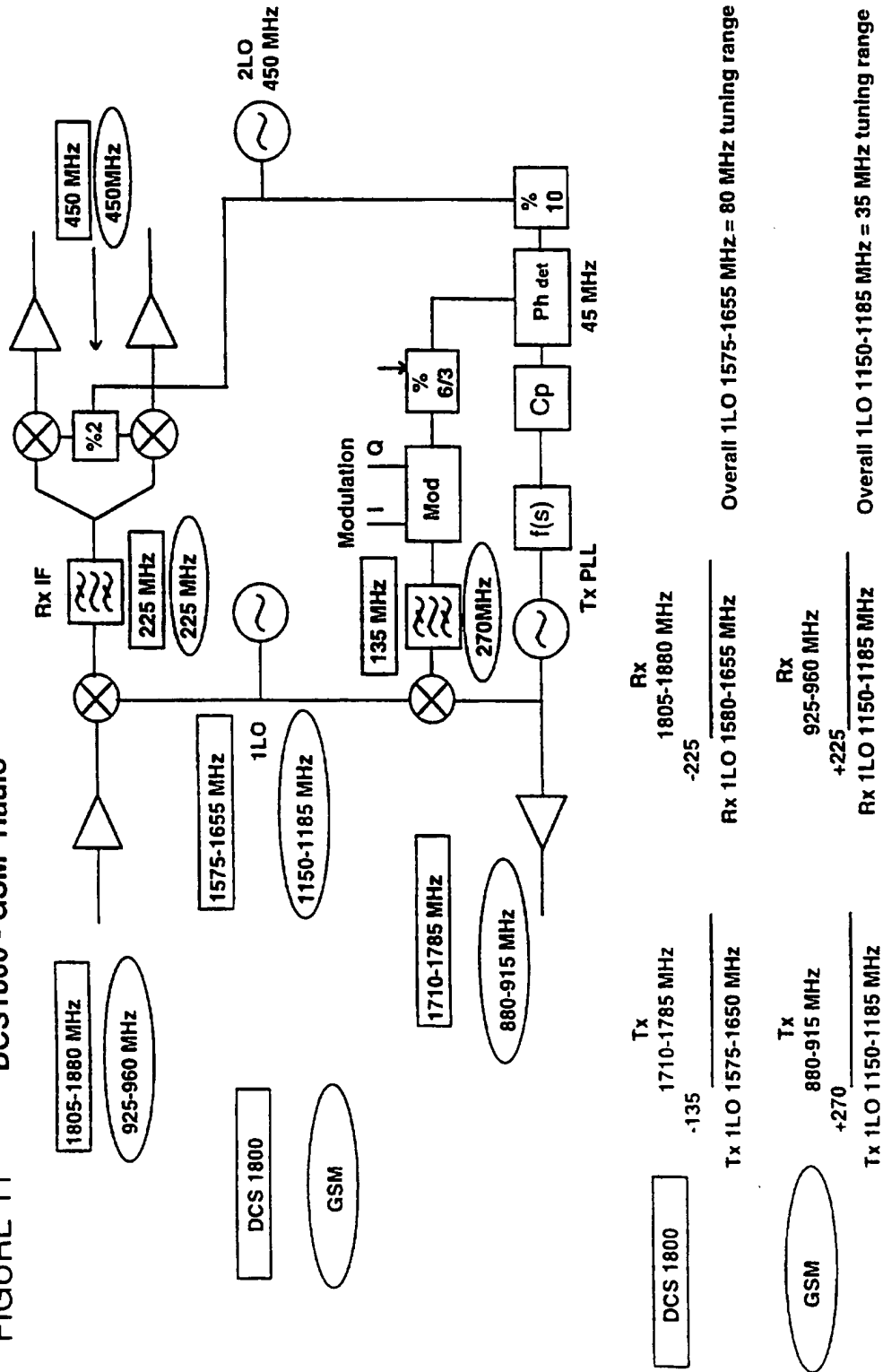


FIGURE 10

DCS 1800 - GSM

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FIGURE 11 DCS1800 - GSM Radio



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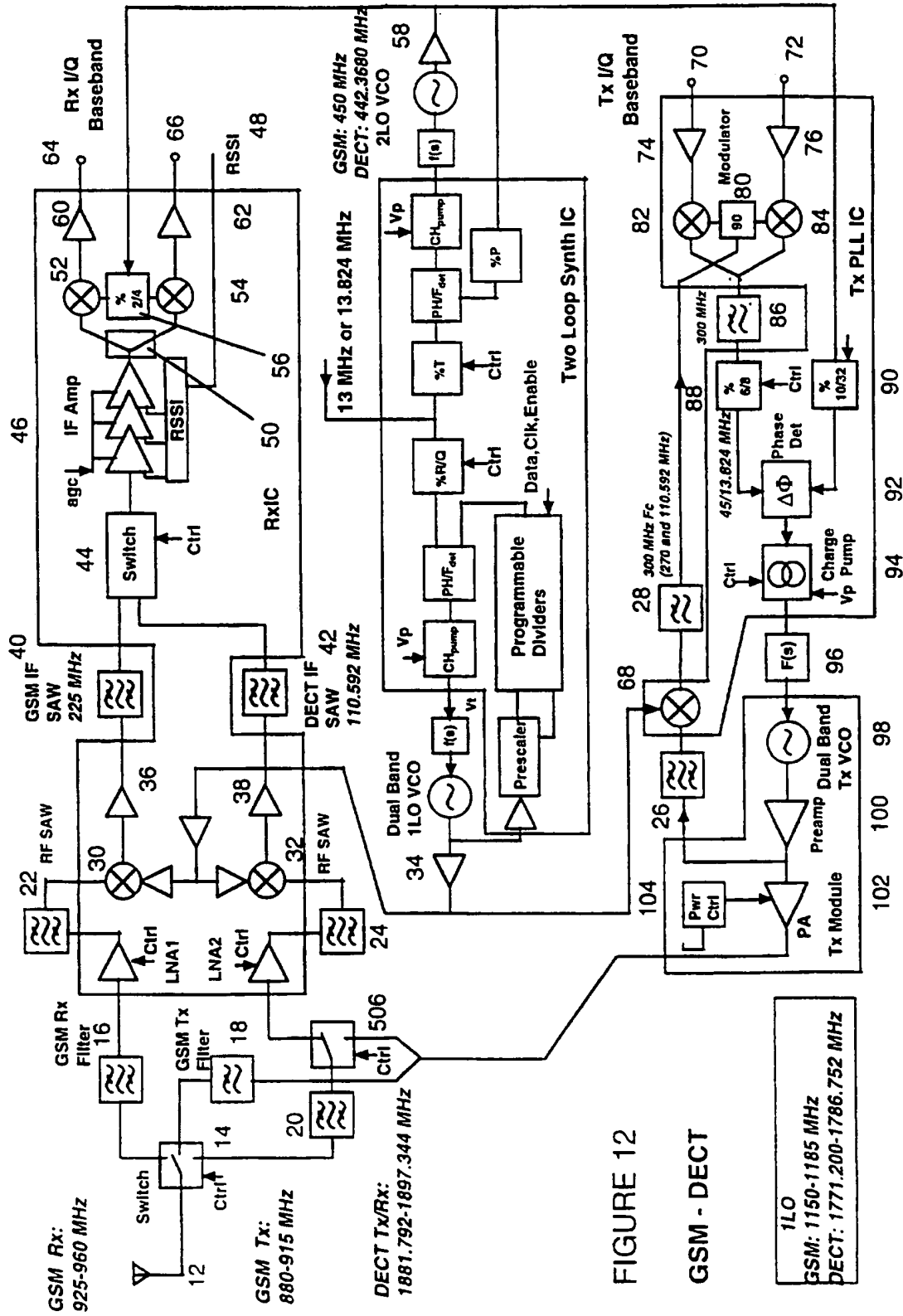
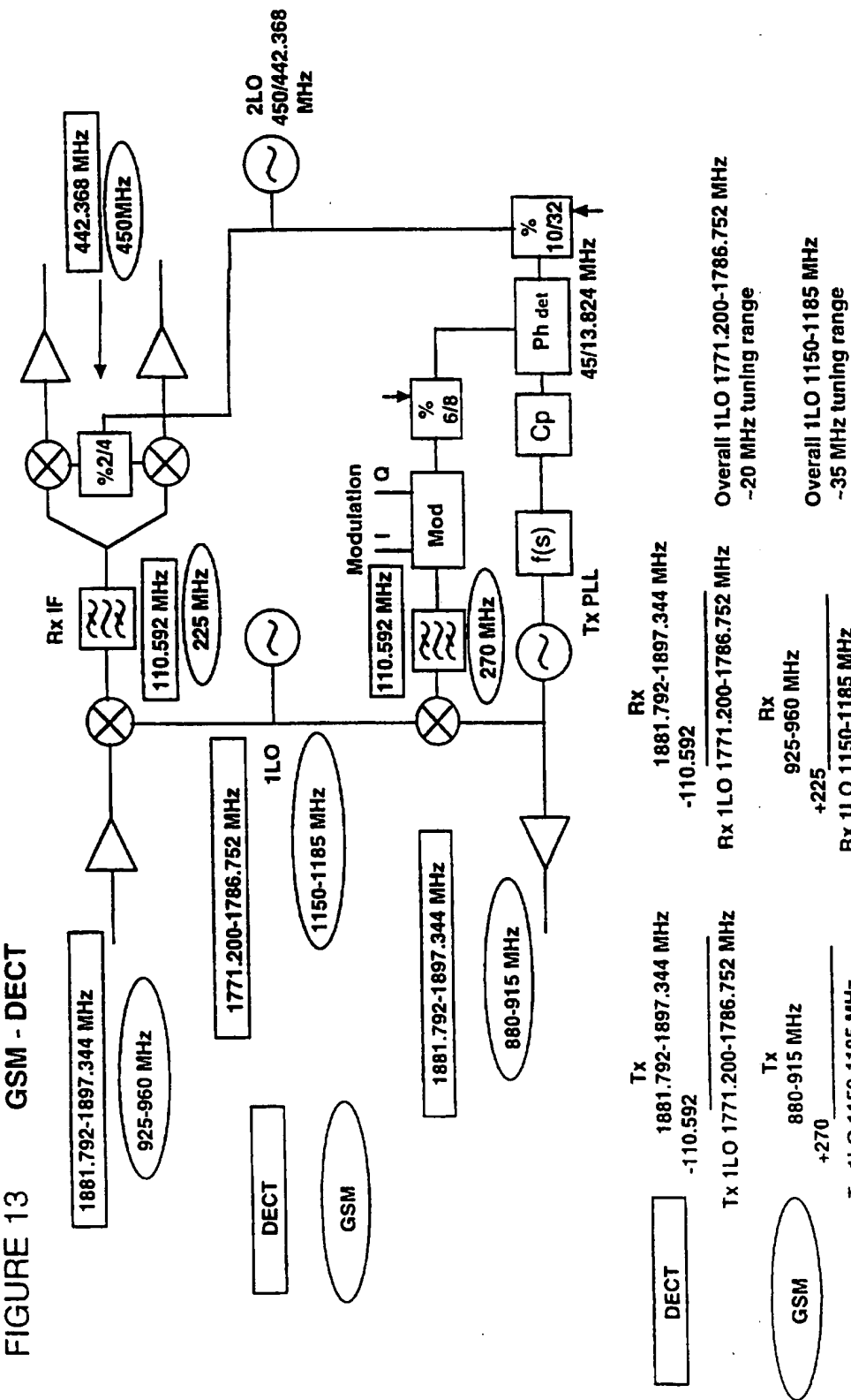


FIGURE 12

GSM - DECT



INTERNATIONAL SEARCH REPORT

Intern al Application No

PCT/GB 97/00422

A. CLASSIFICATION OF SUBJECT MATTER
IPC 6 H04B1/40

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 H04B H04Q H03D

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0 581 573 A (NOKIA MOBILE PHONES LTD) 2 February 1994	1,11-14
A	see page 2, line 21 - line 39; figure 1 see page 3, line 7 - line 14; figure 2 see page 3, line 22 - line 36 see page 5, line 14 - line 16 ---	3
X	EP 0 678 974 A (NOKIA MOBILE PHONES LTD) 25 October 1995	11-14
A	see abstract; figure 2 ---	1,4
A	US 5 406 615 A (MILLER II ROBERT R ET AL) 11 April 1995 see column 1, line 14 - line 43 see column 2, line 33 - line 59 ---	9-14
	--- -/-	

☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

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"A" document member of the same patent family

Date of the actual completion of the international search

23 June 1997

Date of mailing of the international search report

27.06.97

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Goulding, C

INTERNATIONAL SEARCH REPORT

Intern: 11 Application No
PCT/GB 97/00422

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 0 595 781 A (ERICSSON GE MOBILE COMMUNICAT) 4 May 1994 see page 1, line 45 - line 58 ---	1,4
A	WO 92 02991 A (MOTOROLA INC) 20 February 1992 see page 19, line 25 - page 20, line 3; figure 8 ---	1
A	US 5 428 664 A (KOBAYASHI TAKEO) 27 June 1995 see abstract ---	6
A	RF DESIGN, vol. 17, no. 3, 1 March 1994, pages 54, 56, 60-62, XP000445832 SERA M M: "CHIP SET ADDRESSES NORTH AMERICAN DIGITAL CELLULAR MARKET" see page 61, right-hand column, line 16 - page 62, right-hand column, line 17; figure 4 ---	1
A	EP 0 541 305 A (NOKIA MOBILE PHONES LTD) 12 May 1993 see page 2, line 25 - line 48 see page 4, line 17 - line 31 -----	1